INSTRUCTION MANUAL
Digital Multimeter
Model 179/179-20A

COPYRIGHT 1978, KËITHLEY INSTRUMENTS, INC.
THIIRD PRINTING, FEBRUARY 1980, CLEVELAND, OHIO U. S. A.
DOCUMENT NO. 28973

## CONTENTS



## CONTENTS (Cont 'd)

Section Page
7. REPLACEABLE PARTS ..... 7-1
7-1. GENERAL ..... 7-1
7-2. ORDERING INFORMATION ..... 7-1
7-3. MODEL 1789 MAINTENANCE KIT ..... 7-1
7-4. FACTORY SERVICE ..... 7-1
7-5. SCHEMATIC ..... 7-1
7-6. COMPONENT LAYOUT ..... 7-1
7-7. SPECIAL HANDLING OF STATIC SENSITIVE DEVICES ..... 7-1

## ILLUSTRATIONS



## SPECIFICATIONS

## DC VOLTAGE

| RANGE | MAXIMUM READING | $\begin{gathered} \text { ACCURACY }(12 \text { months }) \\ 18^{\circ}-28^{\circ} \mathrm{C} \\ \pm(\% \mathrm{rdg}+\text { digits }) \end{gathered}$ | MAXIMUM ALLOWABLE INPUT |
| :---: | :---: | :---: | :---: |
| 200 mV | 199.99 | 0.04\% + 3d | 1200 V momentary |
| 2 V | 1.9999 | 0.04\% + 1d | 1200V momentary |
| 20 V | 19.999 | 0.04\% + 10 | 1200 V |
| 200 V | 199.99 | 0.04\% + 1d | 1200 V |
| 1200 V | 1200.0 | 0.04\% + 1d | 1200 V |

Temptrature Confficient $\left(0^{\circ}-18^{\circ}\right.$ and $28^{\circ}-55^{\circ} \mathrm{C}$ ):
$\pm 10.006 \%+0.2$ digiti ${ }^{\circ} \mathrm{C}$ excepr $\left.\approx 10.006 \%+0.4 \mathrm{digit}\right){ }^{\circ} \mathrm{C}$ on the 200 mV range.
Input Resstance: $10 \mathrm{~m} \Omega \pm 0.1 \%$

Normal Mode Rejection Ratio
Greater than 60dB at 50 Hz and BOWz . Common Mode Rejection Ratio 11k D unbelencel:

Greater then 120 de at OC .50 Mz and 60 Hz
Setting Tima: 1 seconc to within 1 digut of tinsi resdime

AC VOLTAGE

| RANGE | MAXIMUM READING | ACCURACY (12 months) <br> (abave 2000 counts) $\begin{gathered} 18^{\circ}-28^{\circ} \mathrm{C}: 100 \mathrm{~Hz}-30 \mathrm{kHz} \\ \pm(\% \text { rds }+\mathrm{digits}) \\ \hline \end{gathered}$ | $\begin{aligned} & \text { TEMPERATURE COEFFICIENT } \\ & 0^{\circ}-18^{\circ} \text { and } 28^{\circ}-55^{\circ} \mathrm{C} \\ & \pm 1 \mathrm{~K}^{\circ} \text { ros digita } /{ }^{\circ} \mathrm{C} \\ & 45 \mathrm{~Hz}-10 \mathrm{kHz} \quad 10 \mathrm{kHz}-20 \mathrm{kHz} \end{aligned}$ |  |
| :---: | :---: | :---: | :---: | :---: |
| 200 mV | 199.99 | $0.7 \%$ + 15d | 0.07\% + 2d | 0.15\% - 3d |
| 2 V | 1.9999 | 0.6\% + 15d | 0.07\% + 2d | 0.15\% + 3d |
| 20 V | 19.989 | 0.5\% + 15d | 0.05\% + 2d | 0.05\% + 2d |
| 200 V | 199.99 | 0.5\% + 15d | 0.05\% + 2d | 0.05\% + 2 d |
| 1000 V | 1000.0 | 0.5\% + 15 ${ }^{\text {d }}$ | 0.05\% - 2d | 0.05\% + 2d |

Extended Frequency Accuracy:
$-\{45 \mathrm{~Hz}-100 \mathrm{~Hz}\} \pm\{0.7 \%+15$ dipits $\}$
( $10 \mathrm{kHz}-20 \mathrm{kHz} \div(0.8 \%+15$ digits) on the 20 V end higher
ranges, $-(1.5 \%+$ T 5 digita) on the $2 v$ renge. $\pm 12 \%+15$ digıa)
on the 200 mV range.
Responst: True root mean tauare.
Inpurt Inporance:
$1 \mathrm{M} \Omega \pm 1 \%$ thunted ov lest than $75 \mathrm{p} F$.
Maximum Allomable Input Votrope:
1000 V rms. 1400 V orak, $10^{7} \mathrm{~V}+\mathrm{Mz}$ maximum
Common Mode Rejection Rato ( $1 \times \Omega$ unbelmen):
60 aB at OC. $50 \mathrm{H}_{\mathrm{t}}$ and 80 Mz
Crest Factor: 3.
Settling Time: 2.5 seconds to within 10 digite of inat reseding.
DC AND TRMS AC CURRENT

| RANGE | MAXIMUM READING |  |  | MAXIMUM VOLTAGE BURDEN | SHUNT RESISTANCE |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{aligned} & \text { OC } \quad \begin{array}{c} A C ~ 45 H z-10 k H z \\ \text { tebove } 2000 \text { counti) } \end{array} \end{aligned}$ |  |  |  |  |  |
| 2004 A | 199.99 | 0.2\% + 2d | 1\% + 16 d | 0.2 V | $1 \mathrm{k} \Omega$ |
| 2mA | 1.9999 | 0.2\% + 2d | 1\% + 15 d | 0.2 V | $100 \Omega$ |
| 20 mA | 19.999 | 0.2\% + 2d | 1\% + 15 d | 0.2 V | $10 \Omega$ |
| 200 mA | 199.99 | 0.2\% + 2d | 1\% + 15 d | 0.25 V | $1 \Omega$ |
| 2000 mA | 1999.9 | 0.2\% + 2d | 1\% + 150 | 0.6 V | 0.1 ת |
| $20 \mathrm{~A}^{* *}$ | 19.999 | 0.5\% + 2d | $1 \%+15 d^{*}\{1 \mathrm{kHz}$ max $\}$ | 0.65 V | $0.01 \Omega$ |

*Add 0.1\% rdg above 15A for self-heating

* 20A range on Model 179-20A only.

Tomperature Confficient ( $0^{\circ}-10^{\circ}$ and $29^{\circ}-55^{\circ} \mathrm{C}$ ): $O C \pm 10.01 \%+0.2$ digtts $/ 1^{\circ} \mathrm{C}$.
$A C=10.07 \%+2$.
MAXIMUM INPUT: 2A, 250V DC or rms (fuse protected) except for 20A rang.
15A continuous, 20A for 1 minute (50\% duty cyele). 250 V de or ims fluse protectedt on 20 A range.

Crent Fgats: 3
Sattiong Time: DC: 1 second to within 1 digir of finas reading. AC: 2.5 seconds to within 10 digits of final reading

## RESISTANCE

| RANGE | MAXINIUM READING | $\begin{gathered} \text { ACCURACY }(12 \text { months }) \\ 18^{\circ}-28^{\circ} \mathrm{C} \\ \pm(\% \mathrm{rdg}+\mathrm{digits}) \end{gathered}$ |  | maximum voltage AGROSS UNKNOWN ON RANGE |  | TEMPEAATURE COEFFICIENT $0^{\circ}-18^{\circ}$ and $28^{\circ}-55^{\circ} \mathrm{C}$ $\pm 1 \% \mathrm{rds}+$ digits $/ /^{\circ} \mathrm{C}$ |  | NOMINAL <br> APPIIED CURRENT |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\mathrm{H} 1 \Omega$ | LO 9 | $\mathrm{HI} \mathrm{\Omega}$ | LOI | 배 $\Omega$ | 108 | H10 | L09 |
| 2× 1 | 1.9999 | - | 0.15\% + 15d | - | 0.2 V | - | 0.02\% - 2d | $\checkmark$ | 100 mA |
| $20 \mathrm{k} \Omega$ | 19999 | 0.04\% + id | 0.15\% + 15d | 2 V | 0.2 V | 0.003\% + 0.26 | 0.02\% - 2d | 100mA | 104A |
| $200 * \Omega$ | 199.99 | 0.04\% + id | 0.15\% + 15d | 2 V | 0.2 V | 0.003\% + 0.2 d | 0.02\% + 2a | 10 mA | 1,4A |
| $2000 \mathrm{k} \Omega$ | 1999.9 | 0.04\% + 1d | 0.15\% + 15c | 2 V | 0.2 V | 0.003\% + 0.2 d | 003\% - 2d | 1,1 A | $0115 A$ |
| 20M』 | 19.999 | $0.10 \%+1 d$ | - | 2 V |  | $0.02 \%$ + 0.2 d | - | 0 1HA | - |

Meximum Allowate Input:
450 V ins sustained, ikV OC of paak AC momentery
Setting Time: 1 secand to within 1 digit of tinet cesding exeegr Maximum Open-Circuit Voitage: 5 volts.

2 seconds on the $20 \mathrm{M} \cap$ range.

## GENERAL

DISPLAY: Five 0.5" LED digite, approoriate decimal oosition and Dolarity indication.
CONVERSION PERIOD: 400 milliseconas. ENVIRONMENT:

Operating: $0^{\circ} \mathrm{C}$ to $55^{\circ} \mathrm{C}$
Storage: $-255^{\circ} \mathrm{C}$ to $80 \% 65^{\circ} \mathrm{C}$ Citive numidity vo to $40^{\circ} \mathrm{C}$.
Storage: $-25^{\circ} \mathrm{C}$ to $=65^{\circ} \mathrm{C}$

POWER: $105-125$ or 210-250 valts timiteh refected). 90-110V wallabie. 50-60hz, 7 watts. Optional 6 hour Daterv oack Modet 1788.
DIMENSIONS, WEIGHT: $85 \mathrm{~mm} n \div g n \times 235 \mathrm{~mm}$ wae $\times 275 \mathrm{~mm}$ atere $13-1 / 2 \mathrm{in}$. $9-1 / 4$ in $\times 10-1 / 4 \mathrm{in}$ ।
Net waighr: $1,7 \mathrm{~kg}$. iJ loz. 13 oz
OVEARANGE INDICATION: Oispiav Dlinks all zeros soove 19999 counts
MAXIMUM CONANON MODE VOLTAGE: IU00V oeak


Figure 1-1. Front Panel.

1-1. INTRODUCTION. The Models 179 and 179-20A are versatile digital multimeters useful for measurement of ac and dc voltage, ac and dc current and resistance. The Model 179-20A is identical to the Model 179, except for an added 20 -ampere range. This extra range uses separate input terminals and allows continuous measurement of up to $15 \mathrm{~A} a \mathrm{c} / \mathrm{dc}$, or intermittant duty measurements up to 20A ac/dc. The Model 179-20A is treated by the exception method in this manual. That is, information headed by Model 179-20A applies only to the Model 179-20A. Information headed by Model 179 is common to both the Model 179 and the Model 179-20A, Ranges and accuracies for both models are listed in the Table of Specifications on page $v$. Ranges and functions are selected with front panel pushbuttons. The decimal point is also positioned by the selected range pushbutton. Polarity of the measured signal is automatically displayed.

1-2. WARRANTY INFORMATION. The Warranty is given on the inside front cover of this Instruction Manual. If there is a need to exercise the Warranty, contact the Keithley Representative in your area to determine the proper action to be taken. Keithley maintains service facilities in the United Kingdom and West Germany, as well as in the United States. Check the inside front cover of the Instruction Manual for addresses.

1-3. CHANGE NOTICES. Improvements or changes to the instrument which occur after printing of the Instruction Manual will be explained on a Change Notice sheet attached to the inside back cover.

## IMPORTANT


#### Abstract

The Ssymbol can be found in various places in this Instruction Manual. Carefully read the associated CAUTION statements with regard to proper use and handling of the instrument. Damage to the instrument may occur if these precautions are ignored.

The Ksymbol can be found in various places in this Instruction Manual. This symbol indicates those areas on the instrument which are potential shock hazards. Carefully read the associated WARNING statements with regard to proper use and handiling of the instrument. Serious personal injury may result if these precautions are ignored.




FIGURE 1-2. Dimensional Data



FIGURE 1-3. Tilt Bail Positions.

2-1. GENERAL. This section provides information needed for incoming inspection and prevparation for use.

2-2. INSPECTION. The Model 179 was carefully inspected both mechanically and electrically before shipment. Upon receiving the instrument, check for any obvious damage which may have occurred during transit. Report any damages to the shipping agent. To verify the electrical specifications, follow the procedures given in Section 3 .

2-3. PREPARATION FOR USE. The Model 179 is shipped ready-to-use. The instrument may se powered from line voltage or from rechargeable batteries (when the optional Model 1788 Rechargeable Battery Set is installed).

2-4. OPERATION ON LINE POWER. The Model 179 DMM is provided with a three-wire line cord which mates with third-wire grounded receptacles. Connect the instrument to ac line power as follows:

CAUTION


Connect only to the line voltage selected. Application of incorrect voltage can damage the instrument.
a. Set the LINE VOLTAGE switch on the back of the instrument to correspond to the line voltage available. Ranges are 105 to 125 volts and 210 to 250 volts ac as shown in Figure 2-1.

WARNING
Ground the instrument through a properly grounded receptacle before operation. Failure to ground the instrument can result in severe injury or death in the event of short circuit or malfunction.
b. Plug the power cord into a properly grounded outlet. Operate the 179 DMM as described in SECTION 2-7.


FIGURE 2-1. Rear View Showing Line Switch.

2-5. OPERATION ON BATTERY PACK POWER. The Model 179 DMM may also be operated from rechargeable sealed lead-acid batteries contained in the optional Model 1788 Battery Pack. The Eatery pack will operate the 179 DMM for up to 6 hours. Circuits within the battery pack will automatically shut down the instrument when the battery charge is insufficient to maintain accurate readings. Refer to Figure 2-2 and install the battery pack as follows:

WARNING
Disconnect the line cord before removing the case cover.
a. Turn off the power and disconnect the line cord. Remove four screws from the bottom of the case and separate the top cover from the bottom cover.
b. Lift off the calibration shield, and save it for later use. The four plastic spacers must remain in place on the upright studs projecting through the main circuit board.

## NOTE

Do not discard the calibration shield. This shield must be installed during calibration, as described in Section 4.
c. Set the BAT/LINE switch to the BAT position shown in figure 2-2. Note that the battery pack will not operate properly if this switch is not in the BAT position.
d. Remove fuse F 301 on the battery pack.
e. Install the battery pack in the instrument so that it rests on the plastic spacers. The ground clip must make contact with the upper side of the battery pack plate.
f. Carefully align the battery pack plug with connector plo 04 on the circuit board. Push the plug firmly onto the connector until the lip on the plug engages the lip on the connector to lock the plug in place.

## CAUTION

!Make sure the connector is aligned so that all pins mate properly, otherwise, damage to the DMM will result.
g. Install fuse F301. Reinstall top cover and secure with four screws.
h. Charge the battery pack as described in Paragraph 2-6.

2-6. BATTERY CHARGING. The Model 1788 Battery Pack contains an integral battery charger. To charge or recharge the battery pack, install the battery pack in the 179 DMM as described above and proceed as follows:
a. Connect the instrument to line power as described in Paragraph 2-4.
b. With the power switch off, the battery charge circuitry is automatically energized to charge the battery at the maximum rate. When the battery pack is first installed, or if it has completely discharged, allow it to charge for at least 14 hours in this condition.

NOTE
For maximum battery life, do not allow the battery pack to remain completely discharged. Constant charging will not harm either the battery pack or the instrument.


FIGURE 2-2. Battery Pack Installation
c. When the 179 DMM is in use on line power, the battery charger maintains a trickle charge on the battery pack.

2-7. OPERATING INSTRUCTIONS. Refer to Figure 2-3 and operate the DMM as follows:
a. Turn on the power by depressing the $O N / O F F$ pushbutton.
b. Select the function with the $A C / D C, \Omega, V$, or $A$ pushbuttons.
c. Select the range by depressing the appropriate pushbutton. For resistance measurements only, also set the LO/HI pushbution as desired.
d. Connect the source to the INPUT terminals. Accessories described in Paragraph 2-14 should be used as required.


DCV (200riV, 2V): 450 V rms continuous; 1200 V peak, for 8 seconds per minute. (20-1200V): 1200 V peak.
ACV (All Ranges): 1000 V rms; 1400 V peak; $10^{7} \mathrm{~V} \cdot \mathrm{~Hz}$.
DCA,ACA $(200 \mu A-2000 \mathrm{~mA}): 2 \mathrm{~A}, 250 \mathrm{~V}$ DC or rms (fuse protected)
(20A): 15A continous, 20A for 1 minute ( $50 \%$ duty cycle),
250 V dc or rms (fuse protected)
$\Omega \quad$ (All Ranges): 450 V rms sine wave; 1000 V peak, for 8 seconds per minute. 2-8. DC VOLTAGE MEASUREMENT. Use the Model 179 DMM to measure de volts as follows:
a. Turn on power and set. the $A C / D C$ pushbutton to the out or $D C$ position. Depres the $V$ pushbutton.
b. Select the desired range from the five ranges available. The maximum reading is 19999. Overrange is indicated by a flashing 0000 except on the 1000 volt -ange.

CAUTION
Do not exceed the maximum ratings. Instrument damage may occur.
c. Negative polarity is displayed automatically. \%sive polarity is implied when the minus (-) display is off.
d. Zero the instrument as described in Paragraph 2-14, before the first use whenever the instrument is used outside the temperature range of $18^{\circ}$ to $28^{\circ} \mathrm{C}$, and approximately weekly during normal use.


FIGURE 2-3. Operating Controls.

2-9. AC VOLTAGE MEASUREMENT. Use the Model 179 OMM to measure ac volts as follows:
a. Turn on power and.set the $A C / D C$ pushbutton to the in or $A C$ position. Depress the $V$ pushbutton.

CAUTION
Do not exceed the maximum ratings. Instrument damage may occur.
b. Select the desired range from the five ranges available. The maximum reading is 19999. Overrange is indicated by flashing 0000 except on the 1000 volt range. The instrument measures the true root mean square of a signal within the frequency range of 45 to $20 k$ hertz. Maximum crest factor for rated accuracy is 3.
c. The Model 1682 RF Probe (see Paragraph 2-15) should be used to measure ac vol:ages with a frequency of 20 K to 100 M hertz.
d. Refer to Paragraph 2-13 for TRMS measurements of a signal with both ac and dc components.

2-10. RESISTANCE ( $\Omega$ ) MEASUREMENT. Use the 179 DMM to measure resistance as follows:
a. Turn on power and depress the $\Omega$ pusthbution.

CAUTION
Do not exceed the maximum ratings. Instrument damage may occur.
b. Select the desired range from the five ranges available. The maximum reading is 19999. Overrange is indicated by a flashing 0000. Use the $L 0 / H I$ pushbutton as follows:

1) Use the HI mode for measurements in the $20 \mathrm{k}, 200 \mathrm{k}, 2000 \mathrm{~K}$ and 20 M ohm ranges. Full range voltage drop is 2 volts and is sufficient to cause forward conduction of semiconductor junctions. The HI terminal is positive.
2) Use the $L 0$ mode for measurements in the $2 k, 20 k, 200 k$ and 2000 k ohm ranges. Full range voltage drop is 200 millivolts. Depressing $2 k$ automatically selects LO mode; 20 M selects HI mode. Maximum open circuit voltage is 5 V on all ranges.
c. Zero the instrument as described in Paragraph 2-14 before the first use whenever the instrument is used outside the temperature range of $18^{\circ}$ to $28^{\circ} \mathrm{C}$, and approximately weekly during normal use.

2-11. CURRENT MEASUREMENT (AC or DC). Use the Model 179 or 179-20A to measure ac or dc as follows:

NOTE
To prevent measurement errors when using the Model 179-20A, connect the current test leads to either the $20 A$ jacks or the normal INPUT jacks. Disconnect all circuits from the unused jacks.
a. Turn on power and set the $A C / D C$ pushbutton to the desired $A C$ or $D C$ position. Depress the A pushbutton.

## CAUTION

Do not install larger capacity fuses than those supplied. F102 (2A) and FiO3 (20A, supplied with Model 179-20A only) protect the instrument against overcurrent. Normal acting fuses are used.
b. Select the desired range from the five/six ranges available. (On the Model 179-20A, the $20 \mathrm{~mA} / 20 \mathrm{~A}$ pushbution selects the 20 mA range for the normal $1 N P U T$ jacks and the 20 A range for the 20A jacks). Connect the source to the INPUT jacks for current measurements up to 2000 mA . (For current measurements between 2000 mA and 20 A , connect the source to the 20 A
jacks on the Model 179-20A). The maximum reading is 19999. Overrange is indicated by a flashing 0000. Overload is fuse protected. When using the 20 A current range of the Model 179-20A, up to 15 A may be applied continuously without degradation of the measurement due to self-heating effects. For currents between 15 A and 20A, specified accuracy can only be obtained when measurements are limited to a $50 \%$ duty cycle (i.e., apply the current for a maximum of one minute and then allow at least one minute for cooling before making the next measurement).
2-12. TRMS MEASUREMENT. The Model 179 measures the ac component of a waveform and does not measure the dc component. For ac + dc measurements, use the procedure discussed in a. below.

NOTE
Accuracy is specified for 2000 counts and above. The method of calibrating the converter may yield an offset up to 50 digits with the input shorted. This does not affect the instrument accuracy.
a. Use the 179 DMM to measure TRMS on a signal which has both ac and dc components as follows:

1. Turn on the power. Measure and record the ac and dc components separately.
2. Compute the rms value from the following equation:

$$
E_{R M S}=\sqrt{E_{D C}{ }^{2}+E_{A C}{ }^{2}}
$$

b. The crest factor (CF) is the ratio of the peak voltage to the rms voltage as follows:

$$
C F=\frac{V_{\text {PEAK }}}{V_{\text {RMS }}}
$$

1. Typical crest factors are as follows:

| Sine wave | $C F=\sqrt{2}$ |
| :--- | :--- |
| Square wave | $C F=1$ |
| Triangular wave | $C F=\sqrt{3}$ |
| Positive pulse train | $C F=1 / \sqrt{\text { duty cycle }}$ |
| (duty cycle for $C F=3$ | is 0.11 ) |

## NOTE

There will be some additional measurement error for signals with a crest factor greater than $3(C F>3)$.

2-13. ZERO ADJUSTMENT. The front panel zero adjustment nulls input offset on the 20 , 200 and 1200 de voltage ranges and on all resistance ranges. Typically, this adjustment need not be performed more often than once a week unless the instrument is operated at ambient temperatures outside the range of $18^{\circ}$ to $28^{\circ} \mathrm{C}$. Zero the instrument as follows:
a. Turn on the power and select $L 0 \Omega$ and the 200 k range.
b. Plug in test leads and short them. Adjust the zero adjustment pot (R149) to obtain a reading of $0000 \pm 3$ digits.

NOTE
The zero adjustment may also be used for lead compensation on a particular $\Omega$ range.

2-14. ACCESSORIES. A.wide range of accessories is available to facilitate the use of the Model 179 DMM, extend its range, and adapt it for additional uses.
a. Model 1600 High Voltage Probe. The Model 1600 High Voltage Probe (shown in Figure 7) extends the measurable dc voltage range up to $40 \mathrm{kilovolts} \mathrm{It} \mathrm{has} \mathrm{a} 1000:$.1 division ratio, so that a reading of 1 volt on the DMM corresponds to 1 kilovolt ( 1000 volts). To use the probe, select $D C V$ and the required range, connect the high voltage probe banana plug to the instrument, connect the alligator clip to source low, and touch the probe tip to source high.

## SPECIFICATIONS:

```
Voltage Range: 0 to 40,000 volts \(D C\).
Input Resistance: 1000 megohms.
Division Ratio: 1000:1.
Ratio Accuracy:
\(\pm 1.5 \%\) at 25 kV , decreasing to
\(\pm 2.0 \%\) at 20 kV and 30 kV ,
\(\pm 3.0 \%\) at 10 kV and 40 kV , and
\(\pm 4.0 \%\) at 1 kV .
```

Ratio Stability: $\pm 0.01 \%$ per ${ }^{\circ} \mathrm{C} ; \pm 0.1 \%$ per year.
Heating Effects: Self-heating due to application of high voltage for period in excess of 1 minute will cause a maximum of $0.2 \%$ additional error at 40 kV (error is less at lower voltage).

## WARNING

1
Be sure alligator cilip is connected to source low before touching probe tip to source high. A shock hazard or damage to instrument may result.
b. Model 1651 50-Ampere Shunt. The Model 1651 50-Ampere Shunt (shown in Figure 2-5) permits current measurements from $0-50 \mathrm{~A}$ dc and from $20-50 \mathrm{~A}$ ac. The shunt has a resistance of 0.001 ohm $\pm 1 \%$, so that a 50 -ampere current will correspond to a reading of 50 millivolts ( 0.0500 volt). Set the DMM to ACV or DCV and select the required range. To use the shunt, connect the leads furnished with the shunt from the shunt screw terminals to the DMM input terminals. Use separate leads (not furnished) to connect the source to the hex head bolts. Be sure to use leads with a capacity of $j 0$ amperes, or as needed.
c. Model 1681 Clip-On Test Lead Set. This set (shown in Figure 2-5) contains two leads with banana plugs at one end and spring-action clip-on probes at the other end. plug the leads into the DMM and attach the probes to the source.


FIGURE 2-4. Model 1600 High Voltage Probe.

FIGURE 2-5. Accessories.
d. Model 1683 Universal Test Lead Kit. This kit (shown in Figure 2-5) contains two test leads, 14 tips, two probes, four banana plugs, two spade lugs, and two phone tips to permit connection of the DMM to virtually any source within its range.
e. Model 1682 RF Probe. The Model 1682 RF Probe (stown in Figure 2-6) permits measurement of ac voltages at frequencies of 20 kilohertz to 100 megahertz. Connect the probe to the input terminals and select $A C V$ and the appropriate range.

## SPECIFICATIONS:

Voltage Range: 0.25 to 30 volts rms.
Transfer Accuracy: $\pm 0.5 \mathrm{~dB}, 100 \mathrm{kHz}$ to 100 MHz peak responding calibrated in rms of a sinewave.
Input Impedance: 4 megohm shunted by 3 pF .
Maximum Allowable Input: 30 V rms $A C, 200 \mathrm{~V}$ DC.
Accessories Supplied: straight tip, hook tip, ground clip, hi adapter, banana plug adapter.
f. Model 1685 Clamp-On AC Current Probe. The Model 1685 Clamp-On AC Current Probe (Shown in Figure 2-6) permits measurement of ac current by clamping around a single conductor, eliminating the need to interrupt the current path. Plug the ac current probe into the DMM and select ACV and the appropriate range. The DMM will display 0.1 volt rms per ampere.


Figure 2-6. Model 1682 RF Probe and Model 1685 Clamp-On AC Current Probe.

SPECIFICATIONS:
Range: 2, 20 and 200 amperes rms.
Accuracy: $\pm 4 \%$ of ranges at 60 Hz . $\pm 6 \%$ of range at 50 Hz .
Temperature Coefficient: $\pm 0.05 \% /{ }^{\circ} \mathrm{C}$ on the 20 and 200 ampere ranges. $\pm 0.3 \% /{ }^{\circ} \mathrm{C}$ on the 2 ampere range.
Maximum Allowable Current: 300 amperes rms.
Maximum Conductor Voltage: 600 volts rms.
Conversion Ratio: 0.1 volt rms per ampere.
g. Model 1684 Carrying Case. The Model 1684 Carrying Case (Shown in Figure 2-7) is a hard vinyl case with a fitted foam insert to help protect the 179 DMM from damage. There is also room in the case for the service manual and other small accessories.
h. Models 1010 and 1017 Rack Mounting Kits. The rack mounting kits (shown in figure 2-7) permit mounting one or two Model 179 DMM's $^{2}$ in a rack for convenient viewing.


FIGURE 2 $\quad$ 7. Carrying Case and Rack Mounting Kits.

## SECTION 3. PERFORMANCE VERIFICATION.

3-1. GENERAL. Performance verification should be performed upon receipt of the instrument to ensure that no damage or misadjustment has occurred during transit. Verification may also be performed whenever there is question of the instrument's accuracy, and following calibration, if desired.

NOTE
For instruments that are still under warranty (less than 12 months since date of shipment), if the instrument's performance falls outside specifications at any point, contact your Keithley representative or the factory immediately.

3-2. RECOMMENDED TEST EQUIPMENT. Recommended test equipment for performance verification is listed in Table 3-1. Alternate test equipment may be used. However, if the accuracy of the alternate test equipment is not at least 10 times better than the instrument specifications, additional allowance must be made in the readings obtained.

3-3. ENVIRONMENTAL CONDITIONS. All measurements should be made at an ambient temperature within the range of $18^{\circ}$ to $28^{\circ} \mathrm{C}\left(65^{\circ}\right.$ to $\left.82^{\circ} \mathrm{F}\right)$, and a relative humidity of less than $80 \%$.

3-4. PERFORMANCE VERIFICATION PRODECURE. Use the following procedures to verify the basic accuracy of the Model 179 DMM for voltage, resistance and current measurements. If the instrument is out of specifications at any point, perform a complete calibration as described in Section 4 , unless the instrument is still under warranty, as noted above.

TABLE 3-1.
Recommended Test Equipment For Performance Verification.

| ITEM | DESCRIPTION | SPECIFICATION | MFR. | MODEL |
| :---: | :---: | :---: | :---: | :---: |
| A | DC Calibrator | ```O.1V, 1V, 10V, 100V, 1000V \pm0.002% or 20\muV``` | Fluke | $343 A$ |
| B | AC Calibrator | $\begin{aligned} & 0.1 \mathrm{~V}, 1 \mathrm{~V}, 10 \mathrm{~V}, 100 \mathrm{~V} \\ & \pm 0.022 \% \end{aligned}$ | H-P | 745 A |
| C | AC Calibrator/Amplifier | 1000V @ $\pm 0.04 \%$ | H-P | 745A/746A |
| D | Decade Resistor | $\begin{aligned} & 1.9 \mathrm{~K} \Omega, \quad 19 \mathrm{~K} \Omega, \quad 190 \mathrm{~K} \Omega \\ & 1.9 \mathrm{M} \Omega, 19 \mathrm{M} \Omega, \pm 0.01 \% \end{aligned}$ | ES! | RS 725 |
| $E$ | Current Calibrator | $\begin{aligned} & 100 \mu \mathrm{~A}, 1 \mathrm{~mA}, 10 \mathrm{~mA}, 100 \mathrm{~mA}, \\ & 1 \mathrm{~A}, 10 \mathrm{~A}, \pm 0.03 \% \end{aligned}$ | VALHALLA | 2500 E |

NOTE
Performance verification should be performed by qualified personnel using accurate and reliable test equipment.
a. Initial Conditions. Before beginning the verification procedure the instrument must meet the following conditions:

1) If the instrument has been subjected to extremes of temperature, allow internal temperatures to stabilize for one hour minimum at the environmental conditions specified in Paragraph 3-3.
2) Turn on the 179 DMM and allow it to warm up for 10 minutes. The instrument may be operated from either line power or from battery pack power, as long as the battery pack has been fully charged as described in Paragraph 2-6.
3) Zero the instrument as described in Paragraph 2-14.

WARNING

Some procedures require the use of high voltage. Take care to prevent contact with live circuits which could cause electrical shock resulting in injury or death.
b. $D C$ Volts Checkout.

1) Select dc voltage readings with the $A C / D C$ and $V$ pushbuttons.
2) Connect the DC Calibrator (Item A, Table 3-1) to the instrument.
3) Select the 200 mV range, and apply positive 100 mVdc to the DMM. The reading must be within the limits specified in Table 3-2.
4) Select each remaining range and apply the required voltage as specified in Table 3-2, verify that the reading is within specifications.
5) Repeat all checks with negative voltage.

TABLE 3-2.
DC Voltage Performance Check

| Range | Applied <br> Voltage | Allowable Readings at $18^{\circ}$ to $28^{\circ} \mathrm{C}$ |
| ---: | :--- | :--- |
| 200 mV | 100.00 mV | 99.93 to 100.07 |
| 2 V | 1.0000 V | 0.9995 to 1.0005 |
| 20 V. | 10.000 V | 9.995 to 10.005 |
| 200 V | 100.00 V | 99.95 to 100.05 |
| 1200 V | 1000.0 V | 999.5 to 1000.5 |

c. AC Volts Checkout.

1) Select ac voltage readings with the $A C / D C$ and $V$ pushbuttons.
2) Connect the $A C$ Calibrator (Item B, Table 3-1) to the DMM. Set the calibrator frequency to 1 kHz .
3) Set the DMM to the 200 mV range and apply 100 mV ac to the DMM. The reading must be within the limits specified in Table 3-3.
4) Select the 2,20 and 200 volt ranges and apply the required voltages as specified in Table 3-3. Verify that the readings are within specifications.
5) To check the 1000 volt range, connect the AC Calibrator Amplifier (ltem C, Table 3-1) to the output of the $A C$ Calibrator per the manufacturer's instructions. Set it for an output of 1000 volts ac rms and verify that the DMM readings is within the specified limits.

TABLE 3-3.
$A C$ Voltage Performance Check

| Range | Applied <br> Voltage | Allowable Readings at $18^{\circ}$ to $28^{\circ} \mathrm{C}$ |
| :---: | :---: | :---: |
| 200 mV | 100.00 mV | 99.15 to 100.85 mV |
| 2 V | 1.0000 V | 0.9925 to 1.0075 V |
| 20 V | 10.000 V | 9.935 to 10.065 V |
| 200 V | 100.00 V | 99.35 to 100.65 V |
| 1000 V | 1000.0 V | 993.5 to 1006.5 V |

## d. Resistance Checkout.

1) Select resistance readings by pressing the $\Omega$ pushbutton.
2) Set the $H 1 / L O$ pushbutton to $H I$ and select the $20 \mathrm{k} \Omega$ range.
3) Connect the decade resistor (Item D, Table 3-1) to the DMM.
4) Set the decade resistor to zero and measure the resistance of the test leads. Subtract this reading from the displayed reading in all of the following steps.
5) Set the decade resistor to $19.000 \mathrm{k} \Omega$. Verify that the reading is witnin the limits specified in Table 3-4.
6) Select the next range and measure the next resistance as specified in Table 3-4. Verify that each reading is within specifications. Test each item in the table, switching the $\mathrm{H} \mid / \mathrm{LO}$ pushbutton as indicated.

TABLE 3-4.
Resistance Performance Check

| $\mathrm{HI} / \mathrm{LO}$ | Range | Resistance | Allowable Reading at $18^{\circ}$ to $28^{\circ} \mathrm{C}$ |
| :--- | ---: | ---: | ---: |
| HI | $20 \mathrm{k} \Omega$ | $19.000 \mathrm{k} \Omega$ | 18.990 to $19.010 \mathrm{k} \Omega$ |
| HI | $200 \mathrm{k} \Omega$ | $190.00 \mathrm{k} \Omega$ | 189.90 to $190.10 \mathrm{k} \Omega$ |
| HI | $2000 \mathrm{k} \Omega$ | $1.9000 \mathrm{M} \Omega$ | 1899.0 to $1901.0 \mathrm{k} \Omega$ |
| HI | $20 \mathrm{M} \Omega$ | $19.000 \mathrm{M} \Omega$ | 18.980 to $19.020 \mathrm{M} \Omega$ |
| LO | $2 \mathrm{k} \Omega$ | $1.9000 \mathrm{k} \Omega$ | 1.8957 to $1.9043 \mathrm{k} \Omega$ |
| LO | $20 \mathrm{k} \Omega$ | $19.000 \mathrm{k} \Omega$ | 18.957 to $19.043 \mathrm{k} \Omega$ |
| LO | $200 \mathrm{k} \Omega$ | $190.00 \mathrm{k} \Omega$ | 189.57 to $190.43 \mathrm{k} \Omega$ |
| LO | $2000 \mathrm{k} \Omega$ | $1900.0 \mathrm{k} \Omega$ | 1895.7 to $1904.3 \mathrm{k} \Omega$ |

e. DC Current Checkout:

1) Select dc current readings with the $A C / D C$ and $A$ pushbuttons.
2) Connect the dc current source (Item E, Table 3-1) to the DMM.
3) Select the $200 \mu \mathrm{~A}$ range and apply a current of $100.00 \mu \mathrm{~A}$ to the DMM. The reading must be within the limits in Table 3-5.
4) Select each range and apply the required current as specified in Table 3-5. Verify that the reading is within specifications.
f. Analysis. If the instrument is out of specified limits at any point in Tables 3-2 through 3-5, calibrate the DMM as described in Section 4. If the unit is still under warranty, refer to the note in Paragraph $3-1$.

TABLE 3-5.
DC Current Performance Check

| Range | Applied <br> Current | Allowable Reading at $18^{\circ}$ to $28^{\circ} \mathrm{C}$ |
| ---: | :--- | :--- |
| $200 \mu \mathrm{~A}$ | $100.00 \mu \mathrm{~A}$ | 99.73 to $100.22 \mathrm{\mu A}$ |
| 2 mA | 1.0000 mA | 0.9788 to 1.0022 mA |
| 20 mA | 10.000 mA | 9.978 to 10.022 mA |
| 200 mA | 100.00 mA | 99.78 to 100.22 mA |
| 2000 mA | 1000.0 mA | 997.8 to 1002.2 mA |
| 20 A | 10.000 A | 9.948 to 10.052 A |

## SECTION 4. CALIBRATION

4-1. GENERAL. Calibration should be performed yearly (every 12 months) or whenever performance verification (See Section 3) indicates that the Model 179 DMM is out of specifications. If any step in the calibration procedure cannot be performed properly, refer to Section 5 for Troubleshooting Information or contact your Keithley representative or the factory.

4-2. RECOMMENDED TEST EQUIPMENT. Recommended test equipment for calibration is listed in Table 4-1. Alternate test equipment may be used. However, the accuracy of the alternate test equipment must be at least 10 times better than the instrument specification, or equal to Table $4-1$ specifications.

TABLE 4-1.
Recommended Test Equipment For Calibration.

| Item | Description | Specification | Mfr. | Model |
| :---: | :--- | :--- | :---: | :---: |
| A | DC Calibrator | $0.1 \mathrm{~V}, 1 \mathrm{~V}, 10 \mathrm{~V}, 100 \mathrm{~V}, 1000 \mathrm{~V}$ <br> $\pm 0.002 \%$ or 20 V | Fluke | 343 A |
| B | AC Calibrator | $0.1 \mathrm{~V}, 1 \mathrm{~V}, 10 \mathrm{~V}, 100 \mathrm{~V}$ <br> $\pm 0.022 \%$ | $\mathrm{H}-\mathrm{P}$ | 745 A |
| C | Decade Resistor | $1.9 \mathrm{~K} \Omega, 190 \mathrm{~K} \Omega, \pm 0.01 \%$ | ESI | RS725 |

4-3. ENVIRONMENTAL CONDITIONS. Calibration should be performed under laboratory conditions having an ambient temperature of $20^{\circ}$ to $26^{\circ} \mathrm{C}\left(68^{\circ}\right.$ to $\left.78^{\circ} \mathrm{F}\right)$, and a relative humidity of less than $80 \%$.

4-4. CALIBRATION PROCEDURE. Perform the following adjustments to calibrate the 179 DMM and restore its operation to specified limits.
a. Calibration Shield Installation. If the Model 1788 Battery Pack is installed in the instrument it must be removed and the calibration shield reinstalled before calibration.

HDisconnect the line cord before removing the case cover.

1) Turn off the power and disconnect the line cord. Remove four screws from the bottom of the case and separate the top cover from the bottom cover.
2) Push back the ground clip (shown in Figure 2-2) from the upper side of the battery pack and remove the battery pack from the spacers.
3) Calibration may be performed on battery power as long as the battery pack is sufficiently charged. Leave the battery pack plugged into the instrument, but set the battery pack behind the DMM on the bench or table.
4) Set the calibration shield in place on the spacers. The shield should read correctly when viewed from the front of the instrument.
5) Slide the ground clip over the top of the calibration shield so that it contacts the upper surface of the shield.
6) If battery power-is not to be used, plug in the line cord.
b. Calibration lnstructions.

WARNING

<Some procedures require the use of high voltage. Take care to prevent contact with live circuits which could cause electrical shock resulting in injury or death.

1) Refer to Table 4-2 and perform the listed adjustments in the sequence indicated. Note that the step sequence is also indicated on the calibration shield by boxed numerals. The sequence must be followed exactly because the adjustments are interrelated and dependent on the preceeding steps.
2) If the indicated adjustment cannot be made to obtain the specified reading, refer to Section 2-5 for Troubleshooting Information.

TABLE 4-2.
Calibration Procedure

| Step | Function | Range | Input | Adjustment Point | Desired Reading | Test Equipment* |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | DC V | 2 V | $+1.9 \mathrm{~V}$ | $R 107$ | 1.9000 | DC Calitrator (A) |
| 2 | $D C V$ | 200 mV | $+190 \mathrm{mV}$ | R108 | 190.00 | DC Calibrator |
| 3 | DC V | 2 V | +1.9 V | R107 | 1.9000 | DC Calibrator |
| 4 | ® LO | $200 \mathrm{k} \Omega$ | Short | R149 | Set Front <br> Panel Zero to Mechanical Center. | None |
| 5 | $\Omega 10$ | $200 \mathrm{k} \Omega$ | Short | R112 | $00.0 \pm 10 \mathrm{digits}$ | None |
| 6 | $\Omega$ LO | $200 \mathrm{k} \Omega$ | Short | R149 | $00.00 \pm 2 \mathrm{digits}$ | None |
| 7 | 36 HI | $200 \mathrm{k} \Omega$ | $190 \mathrm{k} \Omega$ | R127 | 190.00 | Decade Resistor (C) |
| 8 | $\Omega$ LO | $2 \mathrm{k} \Omega$ | $1.9 \mathrm{k} \Omega$ | R129 | 1.9000 | Decade Resistor |
| 9 | DC V | 200 V | +190 V | R103 | 190.00 | DC Calibrator |
| 10 | DC V | $20 . \mathrm{V}$ | $+19 \mathrm{~V}$ | R126 | 19.000 | DC Calibrator |
| 11 | DC V | 1000 V | $+1000 \mathrm{~V}$ | R128 | 1000.0 | DC Calibrator |
| 12 | $A C V$ | 20 V | 1 V at I kHz | R142 | 1.000 | AC Calibrator (B) |
| 13 | $A C V$ | 20 V | 10 V at 1 kHz | R143 | 10.000 | AC Calibrator |
| 14 | $A C V$ | 20 V | 1 V at I kHz | R142 | 1.000 | AC Calibrator |
| 15 | $A C V$ | 20 V | 10 V at 1 kHz | R143 | 10.000 | AC Calibrator |
| 16 | $A C V$ | 200 V | 100 V at 10 kHz | C 106 | 100.00 | AC Calibrator |
| 17 | $A C V$ | 20 V | 10 V at 10 kHz | C112 | 10.000 | AC Calibrator |
| 18 | $A C V$ | 2 V | 1 V at 10 kHz | Clll | 1.0000 | AC Calibrator |

[^0]
## SECTION 5. TROUBLESHOOT!NG.

5-1. GENERAL. The troubleshooting instructions contained in this section are intended for qualified personnel having a basic understanding of analog and digital electronic principles and components used in a precision electronic test instrument. Instructions have been written to assist in isolating the defective circuit or subcircuit. |solation of the specific defective component has been left to the technician.

## NOTE

For instruments that are still under warranty (less than 12 months since date of shipment), if the instrument's performance falls outside specifications at any point, contact your keithley representative or the factory immediately.

5-2. TROUBLESHOOTING PROCEDURE. This section contains tables listing sted-by-step checks of the major DMM circuits described in Section 6, Theory of Operation. Proceed as follows:
a. In general, start troubleshooting with Table 5-1, Line Power Checks, to verify that the power supplies are providing the specified voltage to the electronic components.
b. If trouble occurs on battery power only, or if battery operating time is substantially less than 6 hours after overnight charging, test the batteries anc charging circuit per Table 5-2.
c. Proper operation of the $A / D$ converter $\varepsilon$ display should be verified before troubleshooting the signal conditionings. Check these circuits per Tables 5-4 and 5-3, respectively.
d. Problems with ac voltage ranges may involve the ac attenuator, the ac amolifier, or the ac converter. Check these circuits per Table 5-6 and 5-8.
e. Check the de voltage attenuator per Table 5-5 if problems occur with the dc voltage ranges. Check the resistance circuit per Table 5-7 if resistance measurements are erratic.
f. If problems occur with current readings, check the current shunts and related circuits per Table 5-9.
g. All measurements are referenced to analog common (ground clip).

TABLE 5-1.
Line Power Checks

| Step | Item/Component | Required Condition | Remarks |
| :---: | :---: | :---: | :---: |
| 1 | sl01 line switch | Must be set to $105-125 \mathrm{~V}$ or $210-$ 250 V as appropriate. |  |
| 2 | S102 LINE/BAT switch | Must be set to BAT for use with battery pack. |  |
| 3 | Flot line fuse | Continuity. |  |
| 4 | P1007 line cord | Plugged into live receptacle. |  |
| 5 |  | Turn on power. |  |
| 6 | +5V pad; | +5 volts $\pm 10 \%$. | Output of VRIO4. |
| 7 | $\begin{aligned} & \text { VR104, } 1 \mathrm{~N} \\ & \text { C108-2200 } \end{aligned}$ | +7 volts Minimum. | Output of CR101, input to VR104. |
| 8 | +15V pad* | +15 volts $\pm 10 \%$. | Output of VRI02. |
| 9 | TP 1* | +17.5 volts minimum. | Output of CR102, input to VRIO2. |
| 10 | -15V pad* | -15 volts $\pm 10 \%$. | Output of VR101. |
| 11 | TP2* | -17.5 volts minimum. | Output of CRIO2, input to VRIOI. |
|  |  |  | NOTE: Hot regulator may indicate shorted load. |

* On main printed circuit board.

TABLE 5-2.
Battery Power Checks

| Step | I tem/Component | Required Condition | Remarks |
| :---: | :---: | :---: | :---: |
| 1 |  | Check AC line power per Table 5-1. |  |
| 2 |  | Turn off power. |  |
| 3 | S 102 LINE/BAT switch | Move to BAT. |  |
| 4 | Pl007 line cord | Plugged into live receptacle. | Charge circuit checks. |
| 5 | F301 | Remove fuse and connect ammeter to fuse clip. 0 to 500 mA charging rate, varies with line voltage and battery state of charge. | No charge, see step 5A. Correct charging but short battery operating time, see step 6. |
| 5A | BT301 Batteries | Full charge is $=9.8$ volts over 4 cells. R301 adjusts charging rate (float voltage). | If voltage is low and adjustment of R301 does not start charging, see steps 7 and following. If voltage is low and adjustment of R301 does start charging, see Table 5-10 for adjustment of battery charge voltage. |
| 6 | Each battery cell voltage during charging. | Less than 3 volts for any cell. | High voltage or zero indicates damaged cell. |
| 7 | Q301 anode | Full wave rectified voltage, 15 VDC nominal. | Output of CR101. |
| 8. | c304 + | +17.5 volts minimum. | Output of CRIO2. Triggers Q301 gate thru R306 and CR301 unless Q302 is on. |
| 9 | Q302 | Should saturate only when battery approaches full charge. |  |
| 10 | VR301 | 8.2V zener. |  |
| 11 |  | Unplug line cord $\varepsilon$ turn power on. | Discharge checks. |
| 12 | plo04 pin 8 or 0301 pin 11 | 100 kHz 5 V square wave. | Clock input. If no input, see step 12A. |
| 12A | VR104, IN | +7 volts minimum. | Battery voltage inpurt to VRIO4. |
| 13 | Q307 and Q308 base | Square wave, $\pm 0.7$ volts at 25 kHz . | Output of U301, $\div 4$. |
| 14 | Q307, Q308 collector | Must oscillate from saturation to twice battery voltage ( $=19$ volts) at 25 kHz . | Inverter. |
| 15 | C304. C305 | $\pm 17.5$ volts minimum ( $\pm 25$ volts typical with fully charged bat.). | Inverter Output, input to VRIOI \& VRIO2 |

TABLE 5-3.
Display

| Step | 1 tem/Component | Required Condition | Remarks |
| :---: | :---: | :---: | :---: |
| 1 |  | Turn on power. Any function or range except OHMS. |  |
| 2 | +5V\% or J1001, pin 5 | +5 volts $\pm 10 \%$. | If low, check per Table 5-1. |
| 3 | U202, pins 2, 6, 7, 9 and 13 | Digit drive LOW = Enabled. | LED cathode. |
| 4 | $\mathrm{U} 201 \text {, pins } 1,2,6$ and 7 | $H I=$ Enabled. | BCD input to segment decoder/driver. |
| 5 | U201, pin 4 | Positive-going signal lasting for 200 clock pulses. | Leading digit suppression. Output of Ul07A. |
| 6 | J1002, pin 9 | Polarity line (SIGN) HI = off LO = - . | NOTE: Polarity output (in at Jlo02, pin 9) is inverted for VDC on 20 volt and higher ranges. Polarity output is disabled on $A C$ and $\Omega$. |
| 7 | $\begin{aligned} & \mathrm{J} 1001 \text {, pins 1, 2, } 3 \\ & \text { and 4. } \end{aligned}$ | Appropriate DP line high (on). |  |

* On main printed circuit board.

TABLE 5-4.
A/D Converter

| Step | Item/Component | Required Condition | Remarks |
| :---: | :---: | :---: | :---: |
| 1 |  | Turn on Power. Select 2 volt DC range and short inputs. | On this function and range (also on 200 mV DC), input HI connects thru R106, RI36, and RI35G to A/D, without attenuation. |
| 2 | Display | $.0000 \pm 1$ digit. |  |
| 3 | TP 10* | 0.0000 volts. | A/O signal input. |
| 4 | TP8* | +1.00 volt. | Reference output. |
| 5 | TP3* | +100 millivolts. | Reference output. |
| 6 | TP4** | $6.3 \pm 0.25$ volts. | Reference zener voltage. |
| 7 | U106, pin 7 | +1.00 volt. | Reference input to Ul06. |
| 8 | CLK* | 0 to +5 V square wave at 100 kHz . | Clock input. |
| 9 | TP6* | $+1.0 \pm 0.1$ volt. | Stored autozero voltage. |
| 10 | U103, pin 11 | $+1.0 \pm 0.1$ volt. | E-node voltage to integrator in Ul03. |
| 11 | TP 7* | $-1.2 \pm 0.2$ volt. | Ul03 integrator output voltage. |
| 12 | $\begin{aligned} & \text { Ul04, pins } 2,3 \text {, } \\ & \text { and } 6 \end{aligned}$ | +1 volt. | Buffer voltage on Ul04. |
| 13 | External voltage source | Apply +1.9000 volts. Display must read $1.9000 \pm 1$ digit. | Calibration point. |
| 14 | TP7* | Waveform per Figure 5-1. | Integrator output. |
| 15 | Ul03, pin $2^{-}$ | Waveform per Figure 5-1 during ramping of integrator output. | Comparator output. |
| 16 |  | Select 200 mV range and short inputs. |  |
| 17 | Display | $00.00 \pm 3$ digits. | Proceed if out-oflimits. Change selected value of R145 if tests 18-26 meet required conditions. |
| 18 | TP10\% | 0.0000 volts. | A/O signal input. |

* On main printed circuit board.

TABLE 5-4.
A/D Converter, continued

| Step | 1 tem/Component | Required Condition | Remarks |
| :---: | :---: | :---: | :---: |
| 19 | U106, pin 7 | +0.100 volt. | Reference input to U106. |
| 20 | U103, pin 11 | $+1.000 \pm 0.1$ volt. | I-node voltage to integrator in Ul03. |
| 21 | TP7* | $-1.2 \pm 0.2$ volts. | Ul03 integrator output voltage. |
| 22 | U104, pins 2, 3 | +100 millivolts. | Buffer voltage on U104. |
| 23 | U104, pin 6 | +1 volt. | Buffer voltage. |
| 24 | External voltage source | Apply +190 millivolts. Display must read $190.00 \pm 1$ digit. | Calibration point. |

* On main printed circuit board.


FIGURE 5-1. Integrator and Comparator Waveforms.

TABLE 5-5.
DC Volts Attenuator

| Step | I Eem/Component | Required Condition | Remarks |
| :---: | :---: | :---: | :---: |
| 1 |  | Turn on power. Select 20 VDC range, and short inputs. |  |
| 2 | R149 | Front panel adjustment must zero the display. |  |
| 3 | U101, pin 2 | $0.000 \pm 0.005$ volts. |  |
| 4 | External voltage source | Apply +10 valts from HI to LO. | Calibrated input. |
| 5 | TP 5* | -1 voit | Output of 4101. |
| 6 | External voltage source | (Apply +100 and +1000 volts on 200 and 1000 volt ranges. | Calibrated input. |
| 7 | TP5* | -1 volt | Output of UlOI and feedback components, including relays. |

* On main printed circuit board.

TABLE 5-6.
AC Voits Attenuator and $\times 10$ Amplifier

| Step | 1 tem/Component | Required Condition | Remarks |
| :---: | :---: | :---: | :---: |
| 1 |  | Turn on power. Select 2 VAC range. | NOTE: Full scale inputs should produce $\approx 2$ volts output at TPG. |
| 2 | External voltage source | Apply 1 volt rms at 1 kHz . | Calibrated input. |
| 3 | TP9* \& TP5 | 1 volt rms | Output of UlOI and feedback components. |
| 4 | External voltage source | 10,100 and 1000 volts rms on 20,200 and 1000 volt ranges. | Calibrated input. |
| 5 | TP9* \& TP5 | 1 volt rms on all ranges except 200 millivolts. | Output of $U 102$ and feedback components, including relays. |
| 6 | External voltage source | Apply 1, 10, 100V @ 20 kHz on 2,20 and 200 V ranges respectively. | Cl06, Cl11, Cl12, C113, and C114. |
| 7 | External voltage source | Apply 10 V @ 45 Hz on 20 V range. | C105, C115, c116. |
| 8 |  | Select 200 mV range. |  |
| 9 | External voltage source | 100 millivolts at 1 kHz . | Calibrated input. |
| 10 | TP 5* | $100 \mathrm{millivolts} \mathrm{rms}$. | Output of UlOI and feedback components. |
| 11 | TP9: | 1 volt rms. | Output of U102. |

* On main printed circuit board.

TABLE 5-7.
Resistance Circuit.

| Step | Item/Component | Required Condition | Remarks |
| :---: | :---: | :---: | :---: |
| 1 |  | Turn on power. Select $\Omega$, HI and 200 k range. Short inputs. |  |
| 2 | Ul01, pin 2 | Continuity to input HI. | K105. |
| 3 | TP10* | 0.0 volt. | A/D input. |
| 4 | $\Omega$ switch, pin 11 | +1 volt. | Reference voltage. |
| 5 | INPUT HI to LO | Remove short and measure open circuit voltage; must be +2 to +5 volts. | R150 \& R151. |
| 6 | 100k resistor | Apply to input. | Calibrated resistance. |
| 7 | TP10\% | -1 volt. | A/D input. |
| 8 |  | Select Lo range. |  |
| 9 | $\Omega$ switch, pin ll | +100 millivolts. | Reference voltage. |
| 10 | TP10* | - 100 millivolts. | A/D input. |
| 11 |  | Test other ranges in similar manner as needed. | NOTE: Reference loading by the current setting resistor does not affect readout since A/D converter is ratiometric. |

* On main printed circuit board.

TABLE 5-8. AC Converter

| Step | Item/Component | Required Condition | Remarks |
| :---: | :---: | :---: | :---: |
| 1 |  | Turn on power. Select 20 VAC range. |  |
| 2 | External voltage source | 10 volts rms, 1 kHz . | Calibrated input. |
| 3 | TP9* | 1 volt rms (approximate). | Input to converter. |
| 4 | U105, pin 6 or TP $10^{*}$ | +1 volt $D C$. | Output of U105. |
| 5 | R143 | Gain adjustment must operate. |  |
| 6 | Repeat steps 4 \& 5 | 10 volts rms, 45 Hz . | Low frequency response. |
| 7 | Repeat steps $4 \varepsilon 5$ | 10 volts rms, 20 kHz . | High frequency response. |

* On main printed circuit board.

TABLE 5-9.
Current Shunts

| Step | Item/Component | Required Condition | Remarks |
| :---: | :---: | :---: | :---: |
| 1 | F102 | Continuity. |  |
| 2 | $\begin{aligned} & \text { R123, R124, R137, } \\ & \text { R138, R139 } \end{aligned}$ | Correct shunt value for specified range. See schematic. | Measure with ohmmeter. |
| 3 |  | Turn on power. Select DCA and $200 \mu \mathrm{~A}$ range. |  |
| 4 | External voltage source | 0 to 3 volts. | Clamping must occur at $\pm 2$ volts. |

TABLE 5-10.
Adjustment of Battery Charge Voltage

| Step | 1 tem/Component | Required Condition | Remarks |
| :---: | :---: | :---: | :---: |
| 1 |  | Instrument off. |  |
| 2 | R301 | Turn full ccw <br> (maximum charge rate). |  |
| 3 | BT301 | Monitor battery voltage for > 9.8V. | Fully charged cells require several minutes to reach this level. Discharged cells require several hours. <br> ACAUTION: charging to >loV for longer than 30 min . will reduce battery life. |
| 4 | R301 | When cells reach 9.8 V , turn DMM on and adjust to maintain 9.8 V across BT301 |  |

6-1. GENERAL. This section contains circuit descriptions for the Model 179 DMM and for the Model 1788 Battery Pack. An overall block diagram of signal flow is provided in Figure 6-1. The overall schematic diagram, drawing 28992 E , is contained in the back of this manual.


FIGURE 6-1. Simplified Signal Flow Block Diagram, Model 179 DMM

6-2. OVERALL OPERATION. The Model 179 DMM uses a 2-volt or 200-millivolt full scale analog-to-digital (A/D) converter with a $4-1 / 2$ digit multiplexed display. Signal conditioning permits the $A / D$ converter to handle full scale ac and dc voltage and current measurements over 5 decades, and to measure resistance over 5 ranges.
a. Signal Conditioning. Signal conditioning includes dc attenuation (except on the 2 volt and 200 millivolt ranges), ac attenuation and Xlo amplification, ac-to-dc conversion, ohms conversion and current shunts as shown in Figure 6-2.

1) In the DCV mode, signal conditioning to the $A / D$ converter is an active attenuator, except on the two lowest ranges. The $A / D$ input is $-V_{H!-L O} \cdot \frac{R f}{R I}$, except on the lowest ranges or under overload conditions. In the DCA mode, the voltage developed accross the shunt resistor is applied directly to the $A / D$ converter at 200 millivolts full scale.
2) In the $A C V$ mode, ac inputs pass through the attenuator on all ranges. The input is scaled to 2 volts rms full scale, including $\times 10$ amplification for the 200 millivolt range. The TRMS converter outputs a positive dc signal proportional to the true root mean square ac signal. This $D C$ signal is the $A / D$ input. In the ACA mode, shunt voltage is treated as a 200 millivolt signal.
b. Ohms Conversion. Resistance measurements are made by configuring the attenuator as a resistance-to-voltage converter. Attenuator stage voltage feedback resistors $R_{f}$ function as amplifier input resistance connected to either the $0 . i$ volt reference (LO) or the 1.0 volt reference ( HI ). The unknown resistance is connected as a feedback resistor around the attenuation amplifier. The resulting voltage applied to the $A / D$ converter is proportional to the unknown resistance.
c. A/D Converter. The $A / D$ converter is a large scale integration (LSI) ratiometric device. Converter output is a multiplexed 5 digit binary coded decimal ( $B C D$ ) number which is equal to the ratio of input voltage to reference voltage. A separate clock circuit supplies a 100 kHz timing input to the integrated circuit, which also multiplexes the BCD output. Full scale $A / D$ inputs for various ranges and functions are listed in Table 6-1.


FIGURE 6-2. Attenuation and Ohms Conversion.

TABLE 6-1.
Full Scale $A / D$ Inputs.

| Function | Range | Full Scale <br> A/D Input | Reference <br> Voltage |
| :--- | :--- | :--- | :--- |
| DCV | 200 mV | 200 mV | 0.1 V |
| DCV | $2,20,200$ | 2 V | 1.0 V |
| ACV | 1200 V |  |  |
| DCA | All | 2 V | 1.0 V |
| ACA | All | 200 mV | 0.1 V |
| $\Omega$ | HI | 2 V | 1.0 V |
|  | LO | 2 V | 1.0 V |
|  |  | 200 mV | 0.1 V |

6-3. ATTENUATION. When measuring ac and dc voltages, inout signal conditioning is provided by inverting amplifier 4101 and additional componencs as described below.
a. DC. Input resistance is set by R102 and RlO3. During calibration, RlO3 is adjusted to obtain a total input resistance of $10 \mathrm{M} \Omega$. Both fine and coarse zero adjustments are provided since an amplifier output resolution of 10 microvolts is required for Lo resistance measurements.

1) On the 2 volt and 200 millivolt ranges, input $H I$ is connected to the $A / D$ converter through protection resistors R106, R135G and R136. Diode-connected FET's Q106 and Q107 clamp the $A / D$ input during overload.
2) On the 20,200 and 1200 volt ranges, the amount of attenuation is selected by switching feedback resistors into the attenuator with relays K 101 , K 102 and Kl03. Gain setting components and attenuation values are listed in Table 6-2.

TABLE 6-2.
DC Attenuation and Gain Setting Components.

| Range | Gain Set <br> Components | Relay/ <br> Switch | Attenuation |
| :---: | :---: | :---: | :---: |
| 200 mV |  |  | $\left\{\begin{array}{l}\text { Signal bypasses } \\ \text { attenuator }\end{array}\right.$ |
| 2 V |  |  | 0.1 |
| 20 V | R118, R126 | K 101 | 0.01 |
| 200 V | RII9, R127 | K 102 | 0.001 |

b. $A E$ Volts. Inputs resistance is $1 M \Omega$ (RIO1). Shunt capacitance is typicaily less than 75 pF. Additional conditioning is- is follows.

1) For all ranges except the 200 millivolt range, the amount of attenuation is selected by switching feedback resistors into the attenuator with relays KlOl througn K104. For the 200 millivolt range, non-inverting $X 10$ amplifier $U 102$ boosts the signa to 2 volts full scale. Gain setting components and attenuation values are listed in Table 6-3.

TABLE 6-3.
AC Attenuation Gain Setting Components

| Range | Gain Set Components | Relay Energized | Attenuation | Freq. Comp. Capacitors |
| :---: | :---: | :---: | :---: | :---: |
| 200 mV | R118, R126 | K101 | 1 ( $\times 10 *$ ) | C106, C111 |
| 2 V | RII8, R126 | K101 | 1 | C106, C111 |
| 20 V | R119, R127 | K102 | 0.1 | C106, C112 |
| 200 V | R120, R128 | K103 | 0.01 | C106, C113 |
| 1000 V | $\begin{aligned} & \text { R121, R122, } \\ & \text { R129 } \end{aligned}$ | K104 | 0.001 | C106, C114 |

* Signal applied to $\times 10$ ac amplifier Ul02.

2) On the 200 millivolt and 2 volt ranges, high frequency compensation is adjusted with capacitor C111, as shown in Table 6-3. On the 20 volt range, adjustment is performed with C112. On the 200 and 1000 volt ranges, adjustment is performed with clob. Some low frequency rolloff is introduced by input biocking capacitor Cl05, and ac converter input capacitors C 115 and CIl6.

6-4. AC CONVERSION. The ac converter is a monolithic TRMS module. Output $V_{d c}=\sqrt{A v g(V i n)^{2}}$. Potentiometer R143 provides gain adjustment, and R142 establishes output zero. Settling time and ripple are determined by Cllo and C120. Low frequency rolloff is a function of Cl20.

6-5. UAMS CONVERSION. During calibration, the $10 \mathrm{M} \Omega$ input resistance (R102 and R103) and all attenuator feedback resistors are adjusted for both ratio and absolute value. Therefore, these resistors can also serve as reference (ciurrent setting) for resistance measurements. In the $\Omega$ mode, the attenuation (feedback) resistors are disconnected from the output of the attenuation amplifier (U101) and are connected instead to the $A / D$ converter reference voltage. Since two reference voltages and two $A / D$ conve:ter gains are available, the Model 179 DMM provides the option of measuring resistance vi=h the sense current reduced by a factor of 10 .
a. Range Selection. - Operation of the range pushbuttons selects range resisi. to provide the reference current listed in Table 6-4. Operation of the $\mathrm{HI} / \mathrm{LO}$ pushbut. 7 selects the 1 volt or 0.1 volt reference, respectively. Relay KlOS is always energized in the $\Omega$ mode.

TABLE 6-4.
Resistance Range Setting Components

| Range | Range Resistors | Relay/Switch | $\begin{gathered} \text { Nom. IREF } \\ \text { in } \mathrm{HI} \Omega \end{gathered}$ | $\begin{gathered} \text { Nom. } I_{\text {REF }} \\ \text { in LO } 8 \end{gathered}$ |
| :---: | :---: | :---: | :---: | :---: |
| $2 \mathrm{k} \Omega$ | R121, R122, R129 | K104 | - | 100 ${ }_{\mu} \mathrm{A}$ |
| $20 \mathrm{k} \Omega$ | R120, R128 | K103 | 100\%A | $10 \mu \mathrm{~A}$ |
| $200 \mathrm{k} \Omega$ | R119, R127 | K102 | $10 \mu \mathrm{~A}$ | $1 \quad 1$. |
| $2000 \mathrm{k} \Omega$ | R118, R126 | K101 | $1 \mu \mathrm{~A}$ | $0.1 \mu \mathrm{~A}$ |
| $20 \mathrm{M} \Omega$ | R102, R103 | 1000 switch, pins 17, and $\Omega 8,9$. | 0.14 A | - |

b. $\Omega$ Circuit. For resistance measurements, relay KlO 05 and terminals 4. 5, and 6 of the $\Omega$ pushbutton connect the input $H$ lerminal directly to the amplifier summing node. Input LO is disconnected from ground and is connected to the A/D converter input through the protection components described below. The unknown resistance ( $R_{X}$ ) then becomes the amplifier feedback resistance.

1) Current flow in the.unknown resistance is from input Hi to input LO. At full scale, the voltage across $R_{x}$ is either 2 volts (HI) or 200 millivolts (LO). Reference source loading does not affect accuracy since the $A / D$ converter is ratiometric.
2) The HI terminal is clamped to analog common by QlOl and Q102. The instrument protection network at the amplifier output consists of a pulldown resistance (R104 and CRIO3, CR104 and CR105). RlO4 sinks approximately 150 microamps. 'During in-range measurements, this current is supplied by the reference voltage through CRIO5 and voltage through the amplifier (UlO1) and CRIO4. Overloads with input HI positive are sustained by CR105; diodes CR103 and CR104 sustain neqative overloads. Open circuit voltage is set to less than 5 V by R150 and R151 through CR103 and CRIO5. A/D protection in $\Omega$ is the same as in $V$ except RIO5 is substituted for R106.

6-6. A/D CONVERTER. The $A / D$ converter operates on the dual slope principle. The timing is divided into three periods as described below. Operation with high and low reference voltages is described separately in subparagraph d.
a. Auto-Zero. The auto-zero period (A, Figure 6-3) is 100 milliseconds in length, which corresponds to 10,000 clock pulses. During this period, reference voltage $V_{\text {REF }}$ (see subparagraph d) is stored on capacitor C124. Capacitor Cll7 stores VREF ${ }^{+} V_{O S I}-V_{O S} 2$.
b. Signal-Integrate. The signal-integrate period (B, Figure 6-3) is 100 milliseconds in length. The $A / D$ input is buffered by Ul04 (see subparagraph d) and integrated by Ulo3. Positive signals generate a negative-going ramp at the integrator output (pin 14), while negative signals produce a positive-going ramp. The level of the integrated signal at the end of the signal-integrate period is proportional to the average of the applied signal during this period. Since signal integration continues for 100 milliseconds , the A/D converter exhibits high normal mode rejection for ac signals in multiples of 10 hertz, particularly the 50 and 60 hertz line frequencies.
c. Reference-Integrate. The reference-integrate period (C or D, Figure 6-3) is 200 mi Tiseconds or 20,000 counts in length. During this period, the integrator is returned to baseline level by applying a reference voltage of a polarity opposite to that of the signal. A positive-going ramp is obtained by grounding the buffer input, while a negative going ramp is produced by the integration of $2 \times V_{R E F}$ (that is, $V_{R E F}+$ the voltage stored on (124). The time, or number of clock pulses required for discharge is proportional to the signal input. Digital output is from latches within Ul06 which store the number of clock pulses required for the integrator to return to baseline level. The maximum count during this period is 20,000 which corresponds to a discharge period of 200 milliseconds or full scale input.
d. Reference Voltages. Reference voltage $V_{\text {REF }}$ may be either 1 volt or 0.1 volt. Switching through the pushbuttons turns on either Q104 (for 1 volt) or Qlo3 (for 0.1 volt). The voltages are provided by a divider across a temperature compensated zener diode. An operational amplifier on $U 103$ provides the zener with a self-regulating bias. Use of the 0.1 volt reference increases converter sensitivity to 200 millivolts full scale, permitting accurate $L 0$ ohms operation, 10 microvolt resolution on de voltage measurements, and dc amperage measurements with a full scale burden of 200 millivo 'ts. Increased sensitivity is accomplished by switching input buffer 4104 into a gain-of-10 configuration by turning on Q105. Auto-zero charging on C124 is to a 100 millivolt reference instead of a 1 volt reference. Integrator and comparator voltage levels are unaffected by buffer gain. Buffer offset voltage is zeroed, and resistors R146 plus R144 or R145, which are selected at test, null any remaining zero offset on the 200 millivolt range.

6-7. DISPLAY. Five light-emitting diodes (LED) are driven by U201, which is a CMOS BCD-to-seven segment decoder/driver with bipolar current-sourcing outputs. Segment currents are limited to approximately 20 milliamperes peak by resistor network R202. The LED readout is a multiplexed, common-cathode configuration with Darlington array U202 sequentially sinking current from each digit. Blanking of the overrange digit is accomplished by gates U107A and Ul07B. Emitter-follower Q108 ensures that CMOScompatible levels are maintained on Ul07A, pin 1 , regardless of the loading of U202. The minus polarity readout is blanked on ac voltage and resistance ranges by contacts on the pushbutton switch. Proper decimal point position is determined by the combination of function and range selected.

6-8. CURRENT MEASUREMENTS. In the A mode, the signal is switched into one of five current shunts ahead of the attenuator section. For dc current measurements, the shunt voltage drop is applied directly to the $A / D$ converter input at 200 millivolts full scale. For ac current measurements, the shunt voltage drop is treated as a 200 millivolt ac signal and passes through the ac attenuator and the X10 ac amplifier. Overload clamping occurs at three diode drops which is a level high enough io permit high crest factor current waveforms. On the Model $179-20 A$, a sixth current shunt is added and the principle of operation is the same as that described above.

6-9. AC POWER SUPPLY. When the DMM is operated from ac line power, the power supply furnishes $+5,+15$, and -15 volts from regulators VR104, VR102 and VR101, respectively. Full-wave rectified ac from bridge rectifiers CR101 and CR102 is filtered by reservoir capacitors C108, C104 and C103 and is applied to the linear voltage regulators.
A. AUTO-ZERO (1OK COUNTS)

B. SIGNAL INTEGRATE (IOk COUNTS)


FIGURE 6-3. A/D Converter Function (Sheet 1 of 2)
C. NEGATIVE REFERENCE INTEGRATE (2Ok COUNTS AT FULL SCALE) (POSITIVE INPUTS-TO A/D)

D. POSITIVE REFERENCE INTEGRATE (2Ok COUNTS AT FULL SCALE) (NEGATIVE INPUTS TO A/D)


FIGURE 6-3. A/D Converter Function (Sheet 2 of 2)

6-10. MODEL 1788 BATTERY PACK. When the Model 1788 Battery Pack is installed in the DMM, Sl02 must be set to the BAT position to provide additional secondary voltage for battery charging. S 102 also switches the input to VR104 from bridge rectifier CRIOl to batreries BT301. Four 2-volt, 2.5 ampere-hour lead-acid cells supply approximately 9.8 volts at full charge. After six hours of use on battery power, the battery pack should be recharged to ensure long battery life.
a. Battery Charging Circuit. While the DMM is plugged into line power and the battery pack is installed, battery charging proceeds as follows:

1) Full-wave rectified voltage from CR101 is applied to the anode of Q301, which is an SCR which regulates charging voltage. When Q301 is triggered on by a sufficient gate-cathode voltage differential, the batteries receive charge. Charging continues as long as the bridge output voltage exceeds battery voltage by 1 volt or more. Resistor R304 limits charging current when recharging a set of completely discharged cells. A filtered positive output from CR102 (or T301) provides the necessary gate turn-on bias thr 4 R306 and diode CR301. Resistor R303 ensures proper hightemperature operar.. ' of Q301.
2) When the battery voltage reaches the preset float voltage of 9.8 volts, zener VR301 conducts sufficient current to turn on Q302 and thus remise the gate trigger voltage from Q301. Float voltage is adjusted with R301. This is a factory adjustment which normally does not need field readjustment.
b. Battery Operation and Shutdown Circuit. The DMM operates as follows on battery power:
3) When the power is turned on, the batteries are connected to the input of VR104 to supply +5 volts for the logic, display and the clock circuit. The clock output is applied to the $A / D$ converter as described in Paragraph 6-6 and also to U301, which is a divide-by-four binary counter. The outputs of 4301 drive a dc-to-dc inverter which is synchronized to the $A / D$ converter to filter out inverter noise. The 25 kilohertz operating frequency is optimal for the small transformer size, and results in low switching losses. Blocking capacitors C301 and C302 protect'Q307 and Q308 from damage if the drive is lost. Two half-wave rectifiers (CR304 and CR305) on the secondary of T301 provide rectified ac to filter capacitors C304 and C305 which provide power to +15 and -15 volt regulators VR102 and VRIO1.
4) To prevent permanent loss of battery capacity caused by deep discharge, a shutdown circuit stops operation on battery power when the battery voltage drops below approximately 7.2 volts. Shutdown is performed by micropower voltage detector U302. The open-collector output ( 4302 , pin 4) saturates low and turns off pass transistor Q309 when the input voltage (at U302, pin 3) drops below 1.15 volts (typical). Resistor R3l4 provides sufficient hysteresis to prevent discharge from resuming when the battery voltages rises following disconnection of the load.

7-1. GENERAL. This section contains information for ordering replacement parts. The parts list is arranged in alphabetical order of their Circuit Designations.

7-2. ORDERING INFORMATION. To place an order or to obtain information concerning replacement parts, contact you Keithley representative or the factory. See the inside front cover for addresses. When ordering, include the following information:
a. Instrument Model Number.
b. Instrument Serial Number.
c. Part Description'.
d. Circuit Designation (if applicable).
e. Keithley Part Number.

7-3. MODEL 1789 MAINTENANCE KIT. The Model 1789 contains a complement of spare parts that will maintain up to ten Models 178, 179, or 179-20A DMMs (or any combination thereof) for approximately one year. Specify Model 1789 Maintenance Kit when ordering.

7-4. FACTORY SERVICE. If the instrument is to be returned to the factory for service, please complete the Service Form which follows this section, and return it with the instrument.

## 7-5. SCHEMATIC.

a. Model 179 4-1/2 digit TRMS Multimeter: Schematic No. 28992E (Page 7-10). This schematic also describes the Model 1788 Rechargeable Battery Pack.

7-6. COMPONENT LAYOUT.
a. Model 179 4-1/2 Digit TRMS Multimeter (Page 7-11).
b. Model 1788 Rechargeable Battery Pack (Page 7-13).

7-7. SPECIAL HANDLING OF STATIC SENSITIVE DEVICES. CMOS devices are designed to function at very high impedance levels for low power consumption. For this reason, a normal static charge build up on your person or clothing can be sufficient to destrov these devices. The following steps list the static sensitive devices in your Model 179 and provide instruction on how to avoid damaging them when they must be removed/replaced.
a. Static sensitive devices:

Keithley Reference
Part Number Designation

| $I C-102$ | $U 107$ |
| :--- | :--- |
| $I C-103$ | $U 301$ |
| $I C-168$ | $U 201$ |

b. The above integrated circuits should be handled and transported only in protective containers. Typically they will be received in metal tubes or static protective foam. Keep the devices in their original containers until ready for use.
c. Remove the devices from their protective containers only at a properly grounded work bench or table, and only after grounding yourself by using a wrist strap.
d. Handle the devices only by the body. Do not touch the pins.
e. Any printed circuit board into which a device is to be inserted must also be grounded to the bench or table.
f. Use only anti-static type solder suckers.
g. Use only grounded tip soldering irons.
H. After soldering the device into the board, or properly inserting it into the mating receptacle, the device is adequately protected and normal handing can be resumed.

TABLE 7-1.
Cross-Reference of Manufacturers

| $\begin{aligned} & \text { MFR. } \\ & \text { CODE } \end{aligned}$ | NAME AND ADDRESS | FED. SUPPLY CODE | $\\| \text { MFR } .$ | NAME AND ADORESS |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $A-B$ | Allen-Bradley Corp Milwaukee, WI 53204 | 01121 | dLe | Dale Electronics Inc. Columbus, NE 68601 | 91637 |
| A-D | Analog Devices Inc. Norwood, MA 02026 | 24355 | DTN | Dielettron (Consolidated) New York City, NY 10013 |  |
| $A C 1$ | American Components, Inc. Conshohocken, PA 19428 | 14298 | ECI | Electro Cube Inc. <br> San Gabriel, CA 91776 | 14752 |
| AMP | Amphenol <br> Broadview, IL 60153 | 02660 | EDI | Electronic Devices, Inc. Yonkers, NY 10710 | 83701 |
| $A P X$ | Amperex <br> Elkgrove VIg, IL 60007 | 73445 | EFJ | E. F. Johnson Co. Waseca, MN 56093 | 74970 |
| BEC | Beckman Inst. Inc. <br> Fullerton, CA 92634 | 73138 | ERI | Erie Technological Prod. Erie, PA 16512 | 72982 |
| BLD | Belden Mfg. Co. Chicago, IL 60644 | 70903 | F-1 | Fairchild Inst. Corp. Mountaln Vlew, CA 94043 | 07263 |
| BRG | Berg Electronics Inc. New Cumberland, PA 17070 | 22526 | FUS | Bussman Mfg. (Fusetron) St. Louis, MO 63107 | 71400 |
| BRN | Bourns, Inc. Riverside, CA 92507 | 80294 | G-E | General Electric Company Syracuse, NY 13201 | 03508 |
| BUS | Bussman Mfg. Div. St. Louis, M0 63017 | 71400 | G-1 | General Instrument Corp. Newark, NJ 07104 | 72699 |
| C-1 | Components, Inc. Biddeford, ME 04005 | 06751 | GLD | Gould, Inc. <br> St. Paul, MN 55165 | 52431 |
| C-W | Continential-Wirt Elec. Corp. Warminster, PA 18974 | 79727 | $\mathrm{H}-\mathrm{P}$ | Hewlett-Packard Palo Alto, Ca 94304 | 50434 |
| CAD | Caddock <br> Riverside, CA 92507 | 19647 | INT | Intersil inc. Cupertino, CA 95014 | 32293 |
| CAN | ITT Cannon Electric Santa Ana, CA 92702 | 71468 | IRC | IRC Division Burlington, 1 A 52601 | 07716 |
| CLB | Centralab Division Milwaukee, WI 53201 | 71590 | K-1 | Keithley Instruments, Inc. Cleveland, Ohio 44139 | 80164 |
| CLR | Clarostat Mfg. Co., Inc. Dover, NH 03820 | 12697 | $L-F$ | Littlefuse, Inc. Des Plaines, IL 60016 | 75915 |
| CTS | CTS Corporation <br> Elkhart, IN 46514 | 71450 | MOL | Molex <br> Downers Grove, IL 60515 | 27264 |
| DIC | Dickson Electronics Corp. Scottscale, AZ 85252 | 12954 | MOT | Motorola Semi Prod. Inc. Phoenix, AZ 85008 | 04713 |

TABLE 7-1. (Cont'd)

| MFR. CODE | NAME AND ADDRESS | FED. SUPPLY CODE | MFR. CODE | NAME AND ADDRESS | FED. SUPPLY CODE |
| :---: | :---: | :---: | :---: | :---: | :---: |
| NAT | National Semi Corp. Santa Clara, CA 95051 | 27014 | TEP | Tepro Electric Corp. Rochester, NY 14606 | 02985 |
| NCI | National Components, inc. West Palm Beach, FL |  | TPL | Temple <br> Tecate, CA 92080 | 29505 |
| NEL | Northern Engr. Labs <br> Burlington, WI 53105 | 00815 | TRW | TRW Capacitor Div. Ogallala, NB 69153 | 84411 |
| PEB | Potter $\varepsilon$ Brumfield Princeton, IN 47670 | 12300 | vis | Vishay Resistor Products Malvern, PA 19355 | 18612 |
| PAK | Paktron Vienna, VA 22180 |  | VRN | Vernitron Laconia, NH 03246 | 13150 |
| POM | Pomona Electric <br> Pomona, CA 91766 | 05276 | WAB | Wabash-Magnetics <br> Wabash, IN 46992 | 01101 |
| QTN | Q-Tron <br> Santa Ana, CA 92705 | 25525 |  |  |  |
| RAY | Raytheon Company Quincy, MA | 94144 |  |  |  |
| RCA | RCA Corporation Moorestown, NJ 08050 | 02734 |  |  |  |
| RCL | RCL Electronics, Inc. Manchester, NJ 03102 | 01686 |  |  |  |
| SIE | Siemens Corporation <br> Iselin, NJ 08830 | 25088 |  |  |  |
| SIG | Signetics Corp. <br> Sunnyvale, CA 94086 | 18324 |  |  |  |
| SIL | Siliconix lnc. <br> Santa Clara, CA 95054 | 17856 |  |  |  |
| SPG | Sprague Electric Co. Visalia, CA 93278 | 14659 |  |  |  |
| SOL | Solitron Devices Inc. San Diego, CA 92123 | 22229 |  |  |  |
| STD | Standard Condensor Chicago, IL | 97419 |  |  |  |
| T-1 | Texas Instruments, Inc. Dallas, TX 75231 | 01295 |  | . |  |
| TEL | Tel Labs Manchester, NH 03102 | 94322 |  |  |  |

BATTERIES (BT)
Model 1788 Battery Pack
'300'" Series (Sch. 28992E-Pg. 7-15)

| Circuit <br> Desig. | Description | Sch. <br> Location | PC-Board <br> Location | Mfr. <br> Code | Mfr. <br> Desig. |
| :--- | :--- | :--- | :--- | :--- | :--- | | Keithley |
| :---: |
| Part No. |

CAPACITORS (C)
"100" Series (Sch. 28992E-Pg. 7-11)
(PC-Board 492-Pg. 7-13)

| Circuit Desig. | Description $\quad$ Sch. | PC-Board Location | Mfr. Code | Mfr. Desig. | Keithley Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| C101 | 4.74F, 20V, ETT . . . . . . . . 0-7 | D-4 | NCI | KNS 4754020 K | c-179-4.7 |
| C102 | 4.7山F, 20V, ETT . . . . . . . . . D-7 | E-4 | NCI | KNS475A020K | c-179.4.7 |
| C103 | 470uF, 35V, EAL . . . . . . . . . C-8 | E-5 | NIC | 35ELA470 | c-289-470 |
| C104 | 470uF, 35V, EAL . . . . . . . . . -8 | E-5 | NIC | 35ELA470 | c-289-470 |
| C105 | 0.14F, 1000V, MPF . . . . . . . C-3 | F-5 | STO | M2W-F-0. $\mathrm{I}_{\mathrm{L}} \mathrm{F}$ | C-285-. 1 |
| 6106 | .25-1.5pF, 2000V, Teflon Trimmer. . 0-2 | F-4 | EFJ | 273-101 | C184 |
| C107 | $1000 \mathrm{pF}, 500 \mathrm{~V}$, $\pm 5 \%$, Polystyrene . . D-3 | F-4 | CLB | CPR-1000 | c-138-1000 |
| C108 | 2200uF, 15V, EAL . . . . . . . C-7 | 0-3 | NAC | 16 FLA 2200 | C-290-2200 |
| C109 | 3.3pF, $\pm 0.5 \mathrm{pF}, 50 \mathrm{VDC}$, Cero . . . E-2 | E-3 | NAC | DT200-3R3 | C-291-3.3P |
| C110 | IuF, IOOV, $\pm 10 \%$, MPF . . . . . . G-2 | E-3 | POT | 4309 - 105 K | c-294-1 |
| C111 | .25-1.50F, 2000V, Teflon Trimmer. - 0-2 | E-3 | EFJ | 273-1-1 | C-184 |
| C112 | 1.9-15.8pF, 250V, Trimmer . . . . . 0-2 | ع-3 | EFJ | 187-0109-005 | C-284 |
| C113 | 11000pF, 500VDC, $\pm 1 \%$, Silver Mica , D-2 | F-3 | 6-1 | RDM19FD112F03 | C-278-110P |
| C114 | $1100 \mathrm{pF}, 500 \mathrm{VOC}, \pm 1 \%$, Silver Mica. . D-1 | F-3 | G-1 | ROMI9FDIIIFO3 |  |
| C115 | 33uF, 15V, ETT. . . . . . . . . F-2 | E-3 | NCI | KNS 3360015 K | c-228-33 |
| C116 | 33uF, 15V, ETT. . . . . . . . . . F-2 | E-3 | HCI | KNS 3360015 K | C-228-33 |
| 6117 | $1 \pm F, 100 \mathrm{~V}, \pm 10 \%$, MPF. . . . . . J-3 | 0-2 | POT | 4309C-105K | C-294-1 |
| C118 | .22MF, 200VDC, $=10 \%$, MPF . . . . J-2 | 0-2 | POT | 22-200-10-x363um | C-269-. 22 |
| C119 | NOT USED | --- |  |  |  |
| C120 | $14 F, 100 \mathrm{~V}, \pm 10 \%$, MPF . . . . . . F-3 | F-1 | POT | 4309C-105K | C-294-1 |
| C121 | 4.7uF, 20V, ETT . . . . . . . . 0-8 | D-2 | NC! | KNS 4754020 K | c-179-4.7 |
| C122 | 4.7uF, 20V, ETT . . . . . . . . . 0-8 | D-2 | NC1 | KNS 4754020 K | c-179-4.7 |
| C123 | . $1 \mu F, 200 \mathrm{~V}, 20 \%$, MPF. . . . . . G-4 | E-2 | ECI | 62581 Cl 104 | C-221-. 1 |
| ${ }_{C} 124$ | 4uF, 100V, 20\%, MPF . . . . . . H-2 | $E-1$ | POT | 0109-5432 | c-294-4 |
| C125 | 100pF, 1000V, CerD. . . . . . . . F-5 | F-2 | CLB | 00-101 | c-64-100p |

'200'' Series (5ch. 28992E-Pg. 7-11)
(PC-80ard $485-$ Pg. 7-14)

" 300 " Series (Sch. 28992E-Fg. 7-11)
(PC-Board 451-Pg. 7-15)


|  | OIODES (CR) <br> "100" Series (Sch. 28992E-Pg. 7-11) ( PC -8oard $492-\mathrm{Pg}, 7-13$ ) |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Circuit Desig. | Description | Sch. <br> Location | PC-Board <br> Locarion | Mfr. <br> Code | Mfr. <br> Desig. | Keithley Part No. |
| CRIO1 | Bridge Rectifier, $100 \mathrm{~V}, 2 \mathrm{~A}$ | c-6 | D-5 | EDI | PO1O | RF-36 |
| CR102 | Bridge Rectifier, 1A, 400 V | C-7 | 0-5 | EDI | PF40 | RF-46 |
| CR103 | Silicon Rectifier, 1A, 1000V. . . . |  | F-5 | $T-1$ | IN4007 | RF-50 |
| CR104 | Silicon Rectifier, 1A, 1000V. . . . |  | F-5 | $T-1$ | 1N4007 | RF-50 |
| CR105 | Silicon Rectifier, 1A, 1000V. . . . |  | G-5 | $T-1$ | 1N4007 | RF-50 |
| CR106 | Rectifier, $75 \mathrm{~mA}, 75 \mathrm{~V}$. . . . . . . . | 0-8 | 0-4 | $T=1$ | 1 Ng 14 | af-28 |
| CR107 |  |  | 0-2 | $T-1$ | IN914 | AF-28 |
| CR108 | Rectifier, $75 \mathrm{~mA}, 75 \mathrm{~V}$. . . . . . . . J | J-2 | 0-2 | $T-1$ | INSI4 | RF-28 |


| Cireuit Desig. | Description่ | Sch . Location | $\begin{aligned} & \text { PC-soard } \\ & \text { Location } \end{aligned}$ | Mfr. Code | Mfr. Desig. | Keithley <br> Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| C8109 | Rectlfier, $75 \mathrm{~mA}, 75 \mathrm{v}$. | K-3 | F-2 | $T-1$ | 1 NO 14 | RF-28 |
| ¢A110 | Rectifier, 75ma, 75V. . . . . . . | J-3 | F-2 | T-1 | iN914 | AF-28 |
| CRIII | Bridge Rectifier, 5A, 50V . . . . . | A-3 | G-3 | EDI | PE05 | RF-48 |
| CR112 | Rectifier, 3A, 50V. . . . . . . . | A-4 | G-2 | SOL | 3 A 50 | RF-34 |
| CR113 | Rectifier, $75 \mathrm{~mA}, 75 \mathrm{~V}$. . . . . . . | $\mathrm{H}-1$ | C-2 | T-1 | 1 Ng 14 | RF-28 |

'300' Series (Sch, 28992E-Pg. 7-11)
(PC-Board 451-Pg. 7-15)


DISPLAYS (OS)
"200" Series (Sch. 28992E-Pg. 7-11)
(PC-Board 485-9g. 7-14)


FUSES (F)
'100' Series (Sch. 28992E-Pg. 7-11).
(PC-Board 492-Pg. 7-13)

| Circuit Desig. | Description | Sch. <br> Location | PC-Board Location | Mfr. Code | Mfr. Desig. | Keithley <br> Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| F101 | Fuse, sio-8lo, 1/8A, 250V, 3AG. |  | 0-4 | BUS | MDL | FU-20 |
| F102 | Fuse, 2A, 250V, 3AG. |  | F-3 | LIT | 312002 | FU-13 |
| F103* | Fuse, 20A, 250V, 3AB. . | A-5 | 6-2 | LIT | 314020 | FU-47 |

* On Model 179-20A Only
" 300 " Series (Sch. 28992E-Pq. 7-11)
(PC-Board 451-Pg. 7-15)
F3O1 2A, 250V, 3AG, Quick . . . . . F-6 C-3 L-F 312002 FU-13

CONNECTORS (J)
"100" Series (Sch. 28992E-Pg. 7-11)
(PC-8oard 492-Pg. 7-13)


RELAYS (K)
"100" Series (Sch. 28992E-Pg. 7-11)
(PC-Board 492-Pg. 7-13)


|  | $\begin{aligned} & \text { CONNECTORS (P) } \\ & י 1000 " \text { Series (Sch, 28992E-Pg, 7-11) } \\ & \text { (PC-Board 492-Pg. 7-13) } \end{aligned}$ |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Circuit Desig. | Description | Sch. <br> Location | PC-Board <br> Location | Mfr. <br> Code | Mfr. Desig. | Keitnley Part No. |
| P1001 | 6-pin. | H-7 | 6-5 | MOL | 22-03-2061 | C5-247-1 |
| P1002 | 11-Pin. . . . . . . . . . . | . H-6 | G-2 | MOL | 22-03-2061 | CS-347-2 |
| P1003 | male. . . . . . . . . . . . . . . | - 8-8 | 0-5 | MOL | A-2391-3A | ¢5-288-3 |
| P1004 | MALE. | 0-5 | D-3 | MOL | A-2391-8A | 65-288-8 |
| P1005 | NOT USED |  |  |  |  |  |
| $\begin{aligned} & \text { P1006 } \\ & \text { P1007 } \end{aligned}$ | NOT USED |  |  |  |  |  |
|  | Line Cord . . . . . . . . . . | B-8 | --- | K-1 | --- | c0-9 |
|  |  | TRANSISTORS (Q)$\begin{aligned} & " 100 " \text { Series (Sch, 28992E-Pg. 7-11) } \\ &(\mathrm{PC} \text {-6oard } 492-\mathrm{Pg}, 7-13) \end{aligned}$ |  |  |  |  |
| Circuit Desig. | Description | Sch. <br> Location | PC-Board Location | Mfr. Code | Mfr. Desig. | Keithley Part No. |
| Q101 | N-Chan, JFET. | 0-3 | E-4 | INT | 1 TE4392 | TG-77 |
| Q102 | N-Chan, JFET. . . . . . . . . . . | D-3 | E-4 | INT | 1TE4392 | TG-77 |
| Q103 | N -Chan, JFET. . . . . . . . . . . | H-2 | D. 2 | INT | 1 TE4392 | TG-77 |
| Q104 | N-Chan, JFET. . . . . . . . . . . |  | C. 2 | INT | 1 TE4392 | TG-77 |
| Q105 | N-Chan, JFET. . . . . . . . . . . . |  | 0-2 | INT | 1 TE4392 | TG-77 |
| Q106 | N-Chan, JFET. . . . . . . . . . . . | G-3 | E-2 | K-1 | --- | TG-128 |
| Q107 | N -Chan, JFET. . . . . . . . . . . . | . G-4 | F-2 | K-1 | --- | TG-128 |
| Q108 | NPN, 5witeh . . . . . . . . . . |  | G-2 | mot | 2N3904 | TG-47 |
|  | " 300 " Series (Sch. 28992E-Pg. 7-11) (PC-Board $451-\mathrm{Pg} .7-15$ ) |  |  |  |  |  |
| 0304 | Thyristor, SCR | D-6 | C-4 | MOT | 10671 | TG-132 |
| Q302 | NPN, Switch . . . . . . . . . . . . |  | C-3 | MOT | 2N3904 | TG-47 |
| Q303 | NPN, Switeh . . . . . . . . . . . . | E-7 | 0-4 | MOT | 2 N 3904 | T6-47 |
| Q304 | NPN, switch . . . . . . . . . . . . |  | E-4 | MOT | 2N3904 | TG-47 |
| 0305 | NPN, Switch . . . . . . . . . . . . |  | E-4 | HOT | 2N3904 | T6-47 |
| Q306 | PNP, Silicon, T0-92 Case . . . . . |  | E-4 | K-1 | -- | TG-53 |
| Q307 | NPN, Switch . . . . . . . . . . . . |  | E-3 | MOT | 2N3725 | T6-131 |
| Q308 | NPN, Switeh . . . . . . . . . . . . |  | E-3 | HOT | 2N3725 | TG-131 |
| 0309 | PNP, silicon. . . . . . . . . . . |  | 0-3 | Mot | MPS -WAS | TG-133 |
| 0310 | PNP, Silican, T0-92 Case. . . . . . |  | D-3 | k-1 | -19 | TG-53 |
|  | RESISTORS (R) <br> "100" Sarles (Sch. 28992E-Pg. 7-II) (PC-Board 492-Pg. 7-13) |  |  |  |  |  |
| circuit Desig. | Description | Sch. <br> Location | PC-Board Location | Mfr. <br> Code | Mfr. Desig, | Keithley Part No. |
| R101 R102 | 1MR, $00.5 \%, 2 \mathrm{~W}, \mathrm{MtF}$, $200 \mathrm{~V}, \dot{\mathrm{HtF}} \times$. |  | E-4, E-5 | ACI TRW | PME80T9 AR90tio | $\begin{aligned} & R-267-1 M \\ & R-265-9.88 M \end{aligned}$ |
| R103 | 200kn, $10 \%$, Cermet Trlmmer. . . . . |  | F-5 F-5 | TRW | ARgatio 898 | $R-265-9.88 M$ $R P-89-200 \mathrm{~K}$ |
| R104 | 100k』, 10\%, 2w, Comp. . . . . . . . |  | F-5 | A-8 | H8 | R-2-100k |
| R105 | 47kת, 108, 2W, comp . . . . . . . . |  | F-5 | A-B | H8 | R-3-47K |
| R106 | 47Kת, 10\%, 2W, Comp . . . . . . . . |  | 6-5 | A-B | HB | R-3-47K |
| R 107 | 100n, 10\%, Cermet Trimmer . . . . . |  | 0-3 | bra | 30-69-P | RP-64-100 |
| R108 | 200n, 10\%, Cermet Trimmer . . . . . |  | 0-3 | BRN | 30-69-P | R-64-200 |
| 8109 | MATCHED SET WITM YRIOS. . . . . . . |  | D-3 | TRW | MAR 5 | R-263-99.8K (28798A) |
| R110 | MATCHED SET WITH VR105. . . . . . . | J-1 | 0-3 | TRW | MAR-5 | R-263-4.59K (28798A) |
| R111 | 9318. 18, $1 / 8 \mathrm{~W}, \mathrm{MtF}$. . . . . . . . |  | 0-3 | IRC | CEA-TO-931 | R-88-931 |
| R112 | 50kh, 10\%, Cermet Trimmer . . . . . | E-3 | E-3 | BEC | 72PMR | RP-97-50n |
| R113 | 200kn, 1\%, 1/8w, MrF. . . . . . . . |  | E-3 | 1 RC | CEA-TO-200K | R-88-200K |
| R114 | 1.8M, 10\%, 1/4W, Comp . . . . . | D-3 | E-3 | MEP | $\text { CR25, } 5 \%$ | $R-76-1.8 M$ |
| R11 R116 |  | $\mathrm{O}-3$ $\mathrm{E}-2$ | E-3 | IRC TRW | CEA-70-100 | $R-88-100$ |
| R1:7 | 44.9ka, .18, 1/10W, MtF . . . . . . |  | E-3 $\mathrm{E}-3$ | TRW | MAR-5, 113 MAR-5, T13 | $R-263-4.99 k$ $R-263-44.9 k$ |
| R118 | 998kn, . $1 \%, 1 / 4 \mathrm{~W}, \mathrm{MtF}$. . . . . . . |  | E-3 | TRW | МАя-7, Т13 | R-264-998K |
| R119 | $99.8 \mathrm{k} \Omega, .18,1 / 4 \mathrm{~W}, \mathrm{MtF} . \times . \mathrm{}. \cdot$. |  | E-3 | TRW | MAR-5, T13 | R-263-99.8k |
| R120 | 9.98k』, .1\%, 1/10W, MtF . . . . . |  | F-3 | TRW | mar-5, Ti3 | R-263-9.98k |
| R121 | 1.002kS, . $1 \%, 1 / 10 \mathrm{~W}, \mathrm{MtF} . . . .$. |  | F-3 | TRW | MAR-5, T13 | R-263-1.002K |
| R122 | 270kR, 10\%, 1/4W, Comp. . . . . . . | 0-1 | F-3 | MEP | CR25, 5\% | R-76-270k |
| R!23 | . 8980 , .1\%, 5W, WW. . . . . . . . . | 8-4 | F-3 | TEP | TS5-.898 | R-232-. 898 |


|  | RESISTORS (R) (CON'T) <br> "100" Series (Sch. 28992E-Pg. 7-11) <br> (PC-Board 492-Pg. 7-13) |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Circuit Dosig. | Description | Sch. Location | PC-Board Location | Mfr. Code | Mfr . Desig. | Keithley Part No. |
| R124 | .18, .1\%, 7.5W, WW, 5-Terminal . | .. $8-4$ | F-2 | TEL | SPECIAL | R-262-. 1000 |
| R125 | 120n, 10\%, 1/4w, comp . . . . . | . . 6-7 | 6-3 | MEP | CR25, 5\% | R-76-120 |
| R126 | 5K月, 10\%, Cermet Trimmer. . . . . | - $0-2$ | E-3 | BEC | 72PMR | RP-97-5K |
| $R 127$ | 500\%, 10\%, Cermex Trimmer . . . . | - 0-2 | E-3 | BEC | 72PMR | RP-97-500 |
| R128 | 50\%, 10\%, Cermet Trimmer. . . . . | - 0-2 | F-3 | BEC | 72 PMR | RP-97-50 |
| R129 | 50KS, 10\%, Germet Trimner . . . . | . E-1 | F-3 | BEC | 72PMR | RP-97-50K |
| R130 | 143k8, .1\%, 1/10w, MtF. . . . . | - . J-2 | 0-2 | TRW | MAR-5, T13 | A-263-143 |
| R131 | 856Kת, .13, l/low, MtF. . . . . | . . J-2 | 0-2 | TRW | MAR-5, T13 | R-263-856 |
| R132 | 100xn, is, 1/10W, MeF . .. . . | . . J-2 | 0-2 | IRC | CEA-TO-100K | R-88-100K |
| R133 | 26.7h, 1\%, $1 / 8 \mathrm{~W}, \mathrm{MtF}$. | . . j-2 | 0-2 | IRC | CEA-TO-26.7K | R-88-26.7K |
| R134 | 3.018, 1\%, 1/8w, MtF. | . . J-3 | E-2 | IRC | CEA-TO-3.01K | R-88-3.01K |
| R135 ( $\mathrm{A}-\mathrm{j}$ ) | ) Thick Film Network. . | . . SEVERal | E-2 | K-1 | --- | TF-65 |
| 8136 | 47KR, 10\%. Wh, Comp. | - F-4 | F-2 | $A-B$ | HB | R-3-47K |
| R137 | $9 \Omega, 0.5 W, 0.14, W$. . . . . . . | - $8-4$ | F-3 | TEL | SA3 | R-252-9 |
| R138 | 9008, 0.1\%, $1 / 2 \mathrm{~W}, \mathrm{MtF}$. . . . . | - $6-4$ | F-3 | OLE | MFF-1/2-T0-900 | R-169-900 |
| R139 | 90R, $1 / 2 \mathrm{~W}, .1 \%, \mathrm{MtF} . . . . . . . . ~$ | - 8 -4 | F-3 | DLE | MFF-1/2-T0-90 | R-169-90 |
| R140 | $11 \mathrm{~K}, ~ 1 \%, 1 / 8 \mathrm{~W}, \mathrm{MtF}$. . . . . . | - J-1 | 0-2 | IRC | CEA-TO-11K | R-88-1/k |
| R141 | 19.6Kת, 1\%. 1/8W, MtF . . . . . | . J j-2 | 0-2 | 1 RC | CEA-TO-19.6K | R -88-19.6K |
| 8142 | 50K, 10\%, Cermet Trimmer. | - G-3 | E-2 | BEC | 72PMR | RP-97-50K |
| R143 | 500n, 10\%, Cermet Trimmer . . . | - G-2 | E-2 | BEC | 72 PMR | RP-97-500 |
| R144 | Optional, Factory Selected. . . | . $\mathrm{H}-4$ | E-2 | MEP | CR25, 5\% | R-76-: (SEL) |
| 8145 | Optional, Factory Selected. . . . | . $\mathrm{H}-5$ | E-2 | MEP | CR25, 5\% | R-76-* (SEL) |
| 8146 | 18, 5\%, 1/4w, Comp. . . . . . . | . $\mathrm{H}-5$ | E-2 | MEP | CR25, 54 | R-76-1 |
| R147 | 47KR, 10\%, 1/4W, Comp . . . . . | . F-5 | 6-2 | MEP | CR25, 5\% | $\mathrm{R}-76-47 \mathrm{~K}$ |
| R148 | 22M, $10 \%, 1 / 4 \mathrm{~W}, \mathrm{Comp} . . . . . .$. | . F-5 | G-2 | MEP | CR25, 54 | R -76-22M |
| R149 | 200k, 10\%, Cermet Trimmer . . . . | . D-3 | G-1 | BEC | 89 P | RP-89-200K |
| R150 | 3. $3 \mathrm{~K} \Omega, 5 \%, 1 / 4 \mathrm{~W}$, Comp . . . . . | - E-3 | F-5 | MEP | CR25, 5\% | R-76-3.3K |
| R151 | 12K®, 5\%, 1/4W, Comp. . . . . . | - F-3 | F-5 | MEP | CR25, 5\% | R-76-12K |
| R15 2 | (Part of 28798A). . | - J-1 | 0-3 | K-1 | --- | R-88-* |
| R153 | (Part of 28798A). . . . . . . . . | - J-1 | 0-3 | K-1 | --- | R -88-: |
| R154 | .012, .25\%, 7.5W, WW, 4-Terminal | - $\mathrm{x}-1$ | G-2 | TEL | SPECIAL | R-274-. 01 |
| "200" Series (Sch. 28992E-Pg. 7-11) (PC-Biard 492 -pg. 7-13) |  |  |  |  |  |  |
| $\begin{aligned} & \text { R201 } \\ & \text { R202 } \end{aligned}$ | 120. , 1/4W, Comp. . . . | - H-7 | C-2 | MEP | CR25, $5 \%$ | R-76-120 |
|  | 47\%, Thick Film Network. | -J-7 | E-2 | BEC | 899-3-R47 | TF-64 |
|  | " 300 " Series (Sch. 28992E-Pg. 7-11) (PL-Board 451-Pg. 7-15) |  |  |  |  |  |
| $R 301$ $R 302$ | 20kR, $0.5 \mathrm{~W}, \mathrm{pOR}$. . . . . . . . |  | C-3. | BEC | 72PMR-20K | RP-97-20K |
| R302 | 330, $10 \%, 1 / 4 \mathrm{~W}, \mathrm{Comp} \cdot . . . .$. | - $\mathrm{E}-7$ | C-3 | MEP | CR25, 5\% | R-76-330 |
| R303 R304 | $1 \mathrm{~K} \Omega, 10 \%, 1 / 4 \mathrm{~W}, \mathrm{Comp}$ $3.9 \mathrm{~K}, ~ 20 \%, ~ 3 \mathrm{~W}, ~ . ~$ | . $\mathrm{E}-6$ C $\mathrm{D}-6$ | 0-4 | MEP | CR25, 5\% | $R-76-1 \mathrm{~K}$ $\mathrm{R}-268-3.9$ |
| A305 | 4.7kR, $10 \%, 1 / 4 \mathrm{~W}$, comp. . . . . . | - $0-6$ $. E-7$ | D-4 | TEP | TS3 ${ }^{\text {CR25, }}$ 5\% | $R-268-3.9$ $R-76-4.7 \mathrm{~K}$ |
| R 306 | $33 \mathrm{~K}, \mathrm{l}$, 10\%, 1/4W, Comp . . . . . | . $\mathrm{D}-8$ | C-3 | $A-B$ | CB-332-10\% | R-76-3.3K |
| 8307 | 82 K , $, 10 \%, 1 / 4 \mathrm{~W}, \mathrm{Comp}$. . . . . . | . F-7 | E-3 | MEP | CR25, 5\% | R -76-82 |
| R308 | 82K $, ~ 10 \%, 1 / 4 \mathrm{~W}$, Comp . . . . . . | . $\mathrm{F}-7$ | E-3 | MEP | CR25, 5\% | R-76-82 |
| R309 | 100, 10\%, 1/4W, Comp. . . . . . . | - 0.8 | F-4 | A-8 | CB-100-10\% | R-76-10 |
| R310 | 10n, 104, 1/4W, Comp. . . . . . . | - 0-8 | F-4 | A-B | CB-100-10\% | R-76-10 |
| R311 | 100ks, $42,1 / 4 \mathrm{~W}$, comp . . . . . . | . E-6 | E-3 | MEP | CR25, 5\% | R-76-100K |
| R312 | loaka, 5\%, 1/4N, Comp . . . . . . | - E-6 | E-3 | MEP | CR25, 5\% | R-76-100K |
| R313 | 6.8M8, 5\%, 1/4W, Comp . . . . . . | . F-6 | E-3 | MEP | CR25, 5\% | $\mathrm{R}-76-6.8 \mathrm{~m}$ |
| R314 | 6.8ma, 5\%, 1/4w, comp . . . . . . | . F-6 | E-3 | MEP | CR25, 5\% | R-76-6.8M |
| 8315 | $576 \mathrm{ks}, 1 \%$ i\%, $1 / 8 \mathrm{~W}, \mathrm{comp}$. . . . . | . F-6 | E-3 | 1 RC | CEA-TO-576K | R-88-576K |
| R316 | 100 k , 1\%, $1 / 8 \mathrm{w}$, Comp . . . . . | - F-6 | E-3 | IRC | CEA-TO-100K | R-88-100K |
|  | SWITCHES (S) <br> "100" Series (Sch. 28992E-Pg. 7-11) ( PC -Hoard 492-Pg. 7-13) |  |  |  |  |  |
| $\begin{aligned} & \text { Circuit } \\ & \text { Desig. } \end{aligned}$ | Descriprion | Sch. <br> Location | PC-Board Location | Mfr. <br> Code | Mfr. Desig. | Keithley Part No. |
| 5101 b | tine Voltage Selector . . . . . . | - 8-7 | 0.4 | C-W | GG350PCDPDT | SW-318 |
| 5102 L | Line/Battery. . . . . . . . . . . . | - C-6 | D-4 | K-1 | -- | SW-397 |
| 51031 | 11 Station Pushbutton . . . . . . . | - A-5 | G-4 | K-1 | --* | SW-402 (27696A) |

TRANSFORMERS ( T )
' 1001 'Series (Sch. 28992E-Pg. 7-11)

- (PC-Board 492-fg. 7-13)

| Circuit Desig. | Description |  | Sch. <br> Location | pc-Board Location | Mfr. Code | Mfr. Desig. | Keithley Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{aligned} & \text { T101 } \\ & \text { T101 } \end{aligned}$ | Trans former. Trans former, | Power. (i00/200 V). . |  | $\begin{aligned} & 0-5 \\ & 0-5 \end{aligned}$ | $\begin{aligned} & k-1 \\ & x-1 \end{aligned}$ | -- | $\begin{aligned} & T R-168 \\ & T R-169 \end{aligned}$ |
|  |  | "300" Series (Sch. 28992E-Pg. 7-11) ( PC -日card 451-Pg. 7-15) |  |  |  |  |  |
| T301 | Transform | Power. | F-7 | F-3 | $x-1$ | - | TR-170 |

(5ch. 28992E-Pg. 7-11)

integrated circuits (u)
" 100 " Series (Sch. 28992E-Pg. 7-1i)
(PC-8oard 492-Pg. 7-13)

| Circuit Desig. | Description | Sch. Location | PC-Board Location | Mfr. Code | Mfr. Desig. | Keithley Part: No. |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| U101 | Operational AMP 8-Pin. TO-5 | D-3 | E-4 | NAT | LH0022CH | 1c-165 |  |
| U102 | Operational AMP 8-Pin, DiP. . | - E-2 | E-3 | nat | LM30IAN | IC-167 |  |
| U103 | 4-i/2 Digit Analog-Processor. | . K-2 | 0-2 | INT | 8052A | LS 1-12 |  |
| 4104 | Operational AMP 8-Pin, ro-5. | . $\mathrm{H}-2$ | E-2 | NAT | LHOO42 CH | 1c-! 75 |  |
| U105 | TRMS Converter. . . . . . . | - F-2 | 0-2 | A-D | ADS36J | 16-172 |  |
| U106 | 4-1/2 Digit Logic Processor | . . J-4 | E-2 | INT | 7103A | +51++ $\mathrm{CSE}^{-1}$ | 179A-*00 |
| $4107(A-0)$ | 4011 CMOS Unbuffered. | . . SEvERal | G-2 | MOT | MC140116P | 1c-102 |  |

"200" Series (Sch. 28992E-Pg. 7-11)
(PC-Board 485-Pg. 7-14)
U201 Segment Orive . . . . . . . . . . K-7

"300" Series (Sch. 28992E-Pg. 7-11)
(PC-6oard 485-Pg. 7-14)


VOLTAGE REGULATORS (VR)
"100" Series (Sch. 28992E-Pg. 7-11)
(PC-Board 492-Pg. 7-13)

| Circuit Desig. | Description $\begin{aligned} & \text { Sch. } \\ & \text { Location }\end{aligned}$ | PC-Board Location | Mfr. Code | Mfr. Desig. | Xeithley Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| VR101 | -15V, 3-Term . . . . . . . . . . . C-8 | E-5 | MOT | MC7915ct | $1 \mathrm{c}-174$ |
| VR102 | +15V, 3-Term, L0-Power. . . . . . . C-8 | E-5 | MOT | MC78LISCP | 16-170 |
| VR103 | NOT USED |  |  |  |  |
| VRIO4 | +5V, 3-Term, T0-220 . . . . . . . 0-7 | D-4 | MOT | MC78056T | 1-993 |
| VR105 | Reference Zener . . . . . . . . . . J-2 | 0-2 | K-1 | --- | (28798A) |

"300" Series (Sch. 28992E-Pg, 7-11)
(PC-80ard 451-Pg. 7-15)
VR301 8.2 Volt, Zener . . . . . . . . E-6 MOT IN765A OZ-61

CRYSTAL (Y)
"100" Series (Sch, 28992E-Pg. 7-11) (PC-Board 492-Pg. 7-13)

| Circuit Desig. | Description | Sch. <br> tocation | PC-Board Location | Mfr. Code | Mfr. Desig. | Keithley Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Y101 | Quartz, $=0.0$ | F-5 | F-1 | NEL | NE34PE | CR-8 |








1. Describe problem and symptoms using quantitative data whenever possible (enclose readings, chart recordings, etc.)
(Attach additional sheets as necessary).
2. Show a block diagram of your measurement system including all instruments connected (whether power is turned on or not). Also describe signal source.
3. List the positions of all controls and switches on both front and rear panels of the instrument. $\qquad$
4. Describe input signal source levels, frequencies, etc. $\qquad$
$\qquad$
$\qquad$
5. List and describe all cables used in the experiment (length, shielding, etc.).
$\qquad$
$\qquad$
6. List and describe all other equipment used in the experiment. Give control settings for each. $\qquad$
$\qquad$
$\qquad$
7. Environment:

Where is the measurement being performed? (Factory, controlled laboratory, out-of-doors, etc.)
What power line voltage is used? .... Variation? Frequency?
Ambient temperature? ......... Variation? ....... R. Rel. Humidity? Other
8. Additional Information. (If special modifications have been made by the user, please describe below.) $\qquad$


[^0]:    * See Table 4-1.

